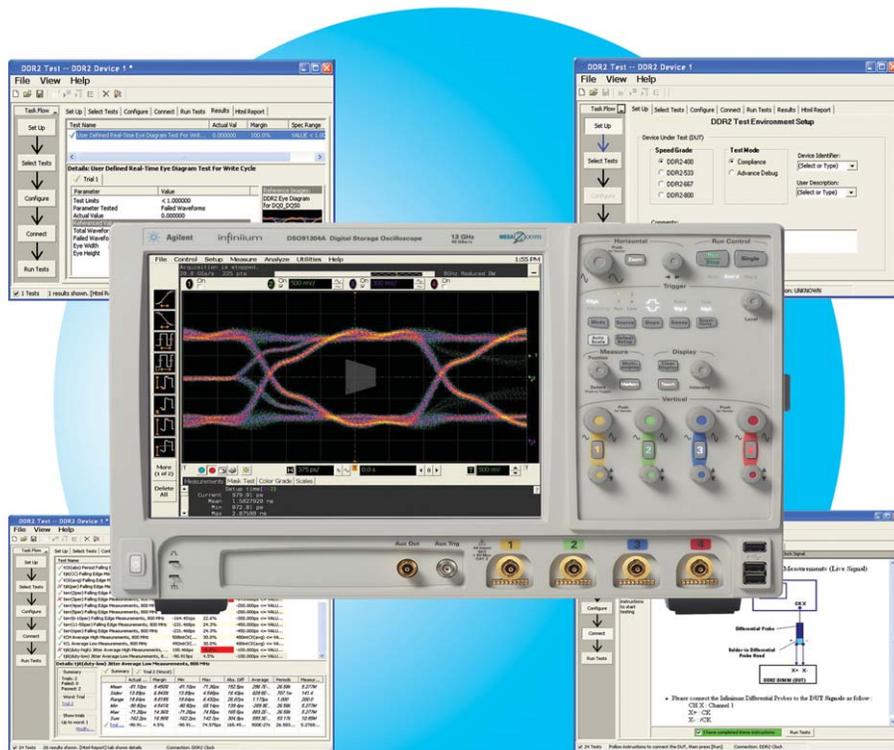


Agilent Technologies N5413B DDR2 and LPDDR2 Compliance Test Application for Infiniium 9000 and 90000 Series Oscilloscope

Data Sheet



Test, debug and characterize your DDR2 and LPDDR2 designs quickly and easily

The Agilent Technologies N5413B DDR2 and LPDDR2 compliance test application provides a fast and easy way to test, debug and characterize your DDR2 and LPDDR2 designs. The tests performed by the DDR2 compliance test software are based

on the JEDEC¹ JESD79-2E DDR2 SDRAM Specification JESD208 Speciality DDR2-1066 SDRAM Specification and Intel® DDR2 667/800 JEDEC Specification Addendum Rev. 1.1. The tests performed by the LPDDR2 compliance test software are based on the JEDEC(1) JESD209-2 LPDDR2 Specification. In addition, both the DDR2 and LPDDR2 test

application features Custom mode, which covers crucial measurements such as eye-diagram, mask testing, ringing and other tests that are not covered in the specifications but are critical for characterizing DDR2 and LPDDR2 devices. The test application offers a user-friendly setup wizard and a comprehensive report that includes margin analysis.

1 The JEDEC (Joint Electronic Device Engineering Council) Solid State Technology Association is a semiconductor engineering standardization body of the Electronic Industries Alliance (EIA), a trade association that represents all areas of the electronic industry.

DDR2 represents an evolutionary upgrade to DDR for memory systems. DDR2 promises higher bandwidth, smaller chip footprints, less power consumption, and less heat generation. DDR2 achieves this with a number of innovations including the use of modern fine ball-grid array (FBGA) packaging, lower supply power and on-die termination for improved control of signal integrity.

LPDDR2 also presents similar technology improvement as DDR2 with significant reduced power by scaling down the voltage of DDR2 from 1.8V to 1.2V

Signal integrity is crucial for memory system interoperability. Reference clock jitter measurements help you ensure that jitter is well within the specifications, which is the key to reliable and interoperable modular memory systems. At the same time, electrical and timing characteristics of signals are critical as well, to ensure the memory system functions correctly and stays error free.

The N5413B DDR2 and LPDDR2 compliance test application is compatible with Agilent 9000 and 90000 Series Infiniium oscilloscopes.

Features

The N5413B DDR2 and LPDDR2 compliance test application offers several features to simplify the validation of your DDR2 and LPDDR2 designs:

- Setup wizard for quick setup, configuration and test
- Execution speed and proven test algorithm for clock test, which minimizes your compliance test time
- User-selected tests and configurations based on JEDEC JESD79-2E and JESD208 DDR2 SDRAM with an option to turn on JESD209-2 LPDDR2 data rate and user-defined speed for embedded designs
- Unique technique to provide read-write burst signal separation on the same bus in real-time mode, allowing powerful debug and analysis
- Ability to analyze the loading effect of adjacent RANK of the same memory channel
- Test framework provides powerful characterization through multiple trials that show a full array of statistics for each measurement and returns the worst measurement value
- Automatically perform derating table calculations for setup and hold time measurements based on slew rate

Comprehensive test coverage

With the DDR2 and LPDDR2 compliance test application, you can use the same oscilloscope you use for everyday debugging to perform automated testing and margin analysis based on the JEDEC electrical and timing specifications. The application automatically configures the oscilloscope for each test and provides informative results. It includes margin analysis indicating how close your device comes to passing or failing the test for each specification.

Some of the difficulties in performing the compliance tests are connecting to the target device, configuring the oscilloscope, performing the tests and analyzing the measured results. The DDR2 and LPDDR2 compliance test application does most of this work for you. If you discover a problem with your device, the Custom mode feature in the test application and debug tools in the oscilloscope are available to aid in root-cause analysis.

Easy test definition

The test application enhances the usability of Agilent Infiniium oscilloscopes for testing DDR2 and LPDDR2 devices. The Agilent automated test framework guides you quickly through the steps required to define the setup, perform the tests and view the test results. On the environmental setup page, you can select the type of DDR2 or LPDDR2 devices, and the framework automatically filters the tests based on your selection. You can then select a category of tests or specify individual tests. The user interface is designed to minimize unnecessary reconnections, which saves time and minimizes potential operator error. You can save the tests and configurations as project files and recall them later for quick testing and review of previous results. Clear menus let you perform tests with minimum mouse clicks.

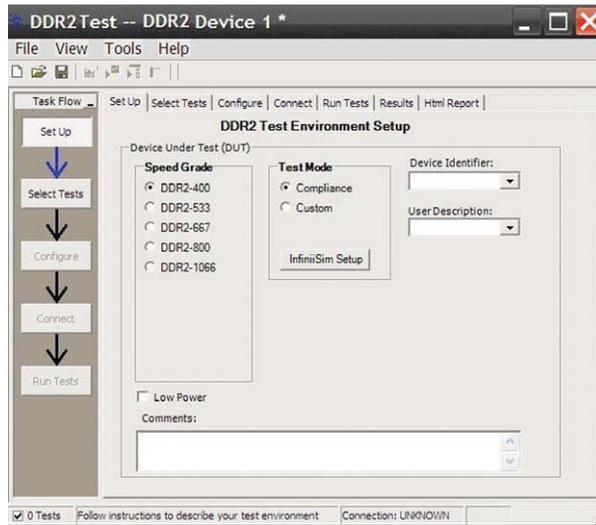


Figure 1. The new test setup screen. Select Compliance or Replace with Custom test mode and the speed grade of your device.

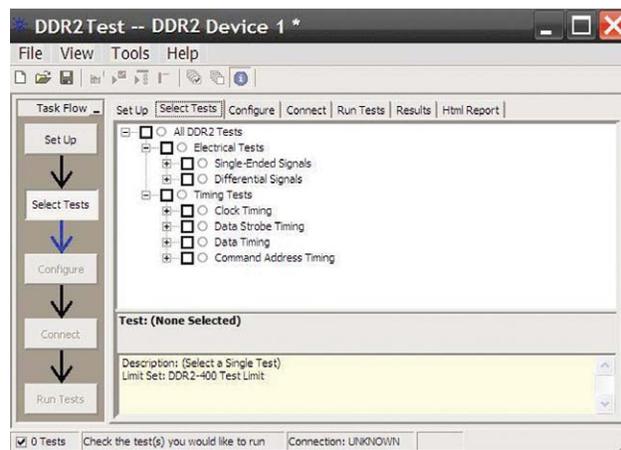


Figure 2. The Agilent automated test engine filters the test selection based on your test setup. You can easily select individual tests or groups of tests with a mouse-click.

Configurability and guided connection

The N5413B DDR2 and LPDDR2 compliance test application provides flexibility in your test setup. The DDR2 and LPDDR2 compliance test application provides you with user-defined controls for critical test parameters such as voltage threshold values, number of waveforms used for analysis and customizable violation settings. Once you have configured the tests, the connection page will display the connection diagram for the test you have selected.

With the multiple test trial capability, you can extensively characterize the performance of your DDR2 devices. You can run the selected tests until the stop condition is met. The application will then save the worst-case conditions and help you track down the anomalies in your signals.

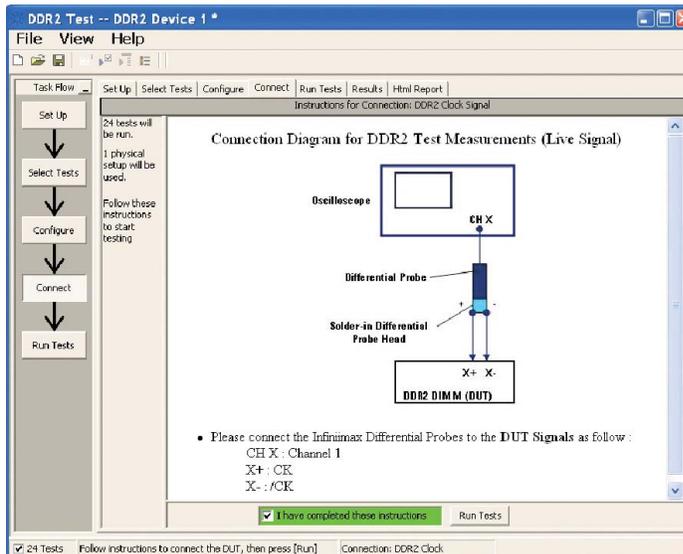


Figure 3. The software prompts you with the connection diagrams for the tests you have selected.

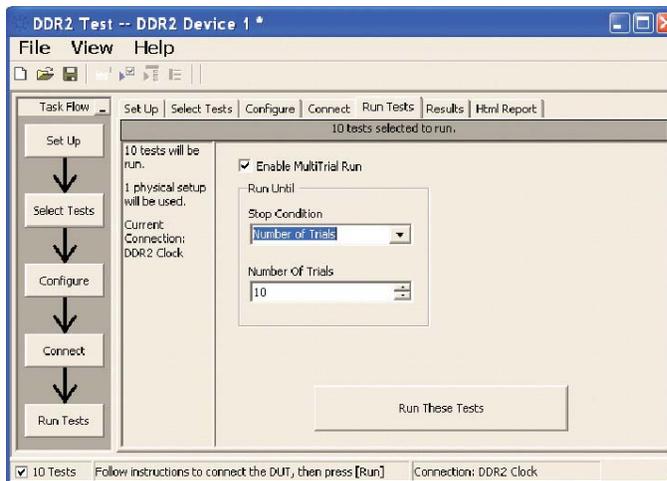


Figure 4. The new Repetitive Run feature allows you to run the selected tests until the stop condition is met. It allows you to extensively test the performance of your device.

In addition to providing you with measurement results, the N5413B DDR2 and LPDDR2 compliance test application reports how close you are to the specified limit. You can specify the level at which warnings are to be issued. You are provided with a full array of statistics for each measurement, and you can save worst-case conditions to extensively test the performance of your device.

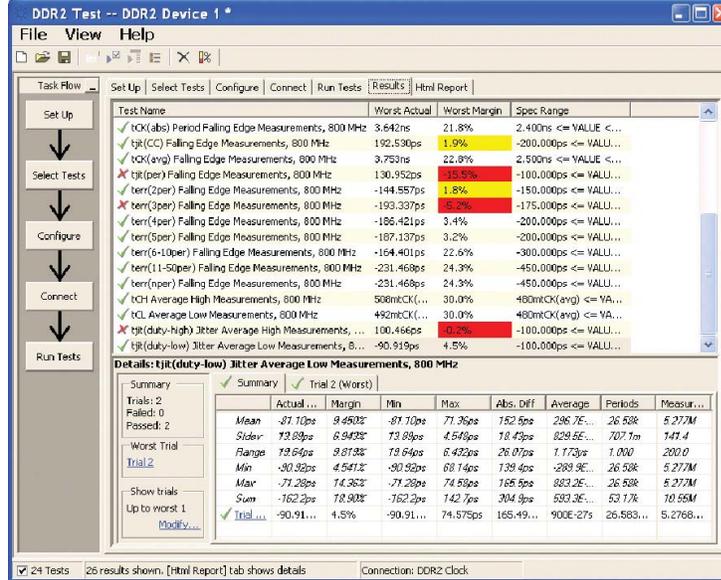


Figure 5. The DDR2 test application documents your test parameters, pass or fail status, test specification range, measured values and the pass/fail margin.

Thorough performance reporting

The N5413B DDR2 and LPDDR2 compliance test application generates thorough HTML reports that capture the performance, status and margins of your device. It also captures screen shots of critical measurements for your reference and documentation. This report is suitable for printing and sharing with your vendors, customers or colleagues.

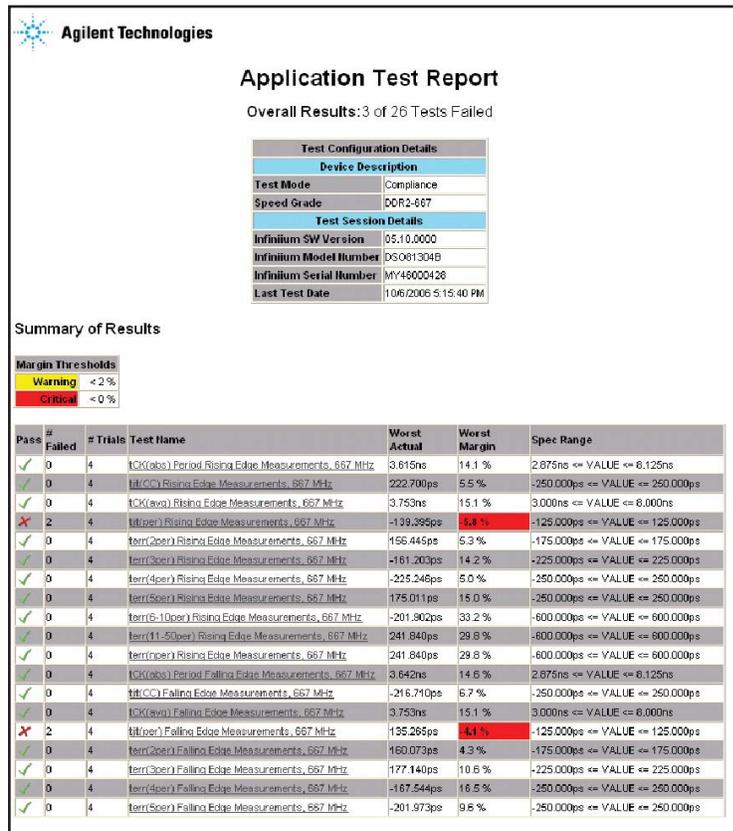


Figure 6. The test application generates a summary report where you can see your device's test results quickly and clearly. Details are available for each test including the test limits, test description and test results, including saved waveforms. In addition, the pass/fail margin is indicated to give you further insight.

System device requirements

In order to speed your test time, you must use the appropriate RAM test reliability software with the memory system to generate random activity on the memory bus. Memtest, is commonly used RAM reliability test software that can run on DOS, Windows® and Linux systems.

Test performed

The Agilent N5413B DDR2 and LPDDR2 compliance test application covers clock, electrical and timing parameters of the JEDEC specifications. The application helps you test all DDR2 and LPDDR2 devices for compliance, using an Agilent 9000 or 90000 Series Infiniium oscilloscope.

In addition, the test application's Custom mode feature provides popular test methodologies that are not covered in any specification. These tests help users who want to perform extensive validation beyond the test specification. It also sets up the scope to isolate the read and write signals so you can immediately jump in to debug the signals.

| Measurement items | Speed supported |
|--------------------------------------|-------------------|
| All JEDEC tests from compliance mode | User configurable |
| Read/write eye-diagram tests | User configurable |

Table 1. Custom mode feature covered by the N5413B test application

Oscilloscope compatibility

The N5413B DDR2 and LPDDR2 compliance test application is compatible with Agilent 9000 and 90000 Series oscilloscopes with operation software revision 2.0 or higher. For oscilloscopes with earlier software revisions, free upgrade software is available at <http://www.agilent.com/find/scope-apps-sw>

| DDR2 and LPDDR2 data rate | Compatible oscilloscope | Bandwidth | Sampling rate |
|---------------------------|-------------------------|-----------|---------------|
| Up to 1066 MT/s | 9254A | 2.5 GHz | 10 GSa/s |
| | 9404A | 4 GHz | 10 GSa/s |
| | 90254A | 2.5 GHz | 20 GSa/s |
| | 90404A | 4 GHz | 20 GSa/s |
| | 90604A | 6 GHz | 20 GSa/s |
| | 90804A | 8 GHz | 40 GSa/s |
| | 91204A | 12 GHz | 40 GSa/s |
| | 91304A | 13 GHz | 40 GSa/s |

Notes:

1. Recommended 4 GHz bandwidth or greater for full characterization.
2. Option 005 noise reduction is recommended for 8-GHz or higher bandwidth oscilloscopes.
3. The JEDEC JESD70 2E, JESD208 and JESD209-2 specifications do not specify the rise time and fall time for DDR2 and LPDDR2 signals. The required oscilloscope bandwidth is also not mentioned. It is advisable for you to determine oscilloscope bandwidth requirement based on the fastest rise time and fall time of the DDR2 and LPDDR2 signals. Please refer to Table 4.

For 9000 and 90000 Series oscilloscope, you can choose the oscilloscope bandwidth using the calculation below.

Maximum signal frequency content = $0.4/\text{fastest rise or fall time (20 - 80\%)}$

Scope bandwidth required = $1.4 \times$ maximum signal frequency for 3% accuracy measurement

Scope bandwidth required = $1.2 \times$ maximum signal frequency for 5% accuracy measurement

Scope bandwidth required = $1.0 \times$ maximum signal frequency for 10% accuracy measurement

| Rise time/fall time | 90254A | 90404A | 90604A | 90804A | 91204A | 91304A |
|---------------------|--------|--------|--------|--------|--------|--------|
| 10 - 90% | 140 ps | 105 ps | 70 ps | 54 ps | 35 ps | 32 ps |
| 20 - 80% | 105 ps | 79 ps | 53 ps | 38 ps | 26 ps | 24 ps |

Table 2. Infiniium 90000A Series oscilloscope rise/fall time specifications

Ordering information

To purchase the Agilent N5413B DDR2 and LPDDR2 compliance test application for your new or existing Infiniium 9000 or 90000 Series oscilloscope, order the following:

| Model number | Description |
|------------------------------|---|
| 9000/90000 | Infiniium Series scope with software 2.0 or higher |
| N5413B | DDR2 and LPDDR2 compliance test application (Option 032 on new 9000 or 90000A Series Oscilloscope -- recommended or Option N5435A-037 for application server license) |
| N5413B-001 | DDR2 and LPDDR2 Compliance Test Application |
| N5413B-002 or N5459A | Upgrade for LPDDR2 Compliance Test Application DDR 1, 2 and 3 Software Bundle Option (Contain options U7233A, N5413B and U7231A) |
| E2688A | High-speed serial data analysis and clock recovery software (Option 003 on new 9000 or 90000 Series oscilloscope or Option N5435A-003 for application server license) |
| N5414A | InfiniiScan event identification software (Option 009 on new 9000 or 90000 Series oscilloscope or Option N5435A-004 for application server license) |
| N5465A | InfiniiSim Waveform Transformation Toolset (Note: This is optional if user does not need to perform probe de-embedding) |
| 116xA/113xA ^{1,2,3} | InfiniiMax I/II probe amplifier (minimum quantity 3 required) |

- 1 Ensure that the probe amplifier meets the bandwidth requirement for your signal measurements. Refer to the "Probe accessories" section on page 10 to configure the probe head to go with your probe amplifier.
- 2 For multiple RANK testing, a quantity of 4 probes are required for additional probing of Chip Select (CS) pin.
- 3 Use 113xA probe amplifier or add N2880A in-line attenuator when use with 116xA probe amplifier if measured signal is above 1.65V.

Probe accessories

InfiniiMax probe amplifiers

| Model number | Description |
|--------------|--------------------------------------|
| 1169A | 12-GHz differential probe amplifier |
| 1168A | 10-GHz differential probe amplifier |
| 1134A | 7-GHz differential probe amplifier |
| 1132A | 5-GHz differential probe amplifier |
| 1131A | 3.5-GHz differential probe amplifier |
| 1130A | 1.5-GHz differential probe amplifier |

InfiniiMax probe heads

| Model number | Description |
|--------------|--|
| N5381A | InfiniiMax II 12-GHz differential solder-in probe head and accessories |
| N5382A | InfiniiMax II 12-GHz differential browser |
| E2677A | InfiniiMax 12-GHz differential solder-in probe head and accessories |
| E2675A | InfiniiMax 6-GHz differential browser probe head and accessories |
| N5425A | InfiniiMax 12-GHz ZIF Probe Head |
| N5426A | ZIF Tips (x10) |
| N5451A | Long Wire ZIF Tips (x10) |
| E2678A | InfiniiMax 12-GHz differential socket probe head and accessories |

DDR2 BGA Probe

| Model number | Description |
|--------------|---|
| W2631B | DDR2 x16 BGA command and data probe |
| W2632A | DDR2 x16 BGA data probe |
| W2633B | DDR2 x8 BGA command and data probe |
| W2634A | DDR2 x8 BGA data probe |
| W2639A | Scope adapter board for DDR2 BGA probe - Kit of 2 (Note: For use with E2678A differential socket probe head and accessories) |

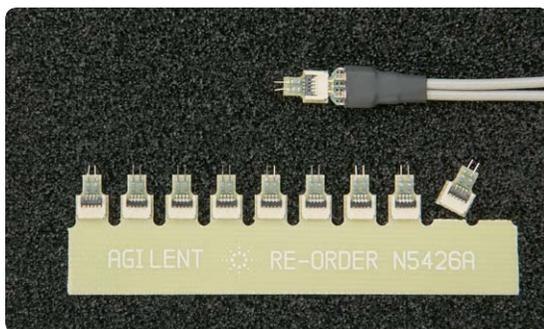


Figure 7. New InfiniiMax ZIF probe head (N5425A) and ZIF tips (N5426A) for your DDR2 probing solution.

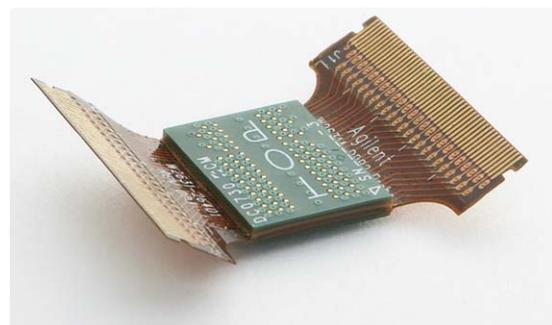


Figure 8. DDR2 BGA probe adapter for oscilloscopes and logic analyzers

Related literature

| Publication title | Publication type | Publication number |
|---|------------------|--------------------|
| <i>Agilent Infiniium DSO/DSA 90000A Series Oscilloscopes and InfiniiMax Series Probes</i> | Data Sheet | 5989-7819EN |
| <i>Agilent InfiniiScan Event Identification Software for Infiniium Series Oscilloscopes (N5414A and 5415A)</i> | Data Sheet | 5989-4605EN |
| <i>Agilent Technologies E2688A, N5384A High-Speed Serial Data Analysis and Clock Recovery Software for Infiniium Series Oscilloscopes</i> | Data Sheet | 5989-0108EN |
| <i>Agilent Technologies EZJIT and EZJIT Plus Jitter Analysis Software for Infiniium Series Oscilloscope</i> | Data Sheet | 5989-0109EN |
| <i>W2630 Series DDR2 BGA Probe for Logic Analyzers and Oscilloscopes</i> | Data Sheet | 5989-5964EN |
| <i>W2637A, W2638A and W2639A LPDDR BGA Probes for Logic Analyzers and Oscilloscopes</i> | Data Sheet | 5990-3894EN |
| <i>A Time-Saving Method for Analyzing Signal Integrity in DDR Memory Buses</i> | Application Note | 5989-6664EN |

To download copies of these publications, go to www.agilent.com/find/N5413B



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