



**Agilent Technologies**

# Oscillator DesignGuide

**August 2005**

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# Chapter 1: Oscillator QuickStart Guide

The Oscillator QuickStart Guide serves as a simple introduction to using the Oscillator DesignGuide. For more detailed reference information, refer to [Chapter 2, Oscillator DesignGuide Reference](#).

The DesignGuide is applicable to any oscillator, but is especially useful for RF Board and Microwave applications. It is designed to help both experts and novices to create designs of various complexity.

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**Note** This manual is written describing and showing access through the cascading menu preference. If you are running the program through the selection dialog box method, the appearance and interface will be slightly different.

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## Using DesignGuides

All DesignGuides can be accessed in the Schematic window through either cascading menus or dialog boxes. You can configure your preferred method in the Advanced Design System Main window. Select the *DesignGuide* menu.

The commands in this menu are as follows:

**DesignGuide Studio Documentation > Developer Studio Documentation** is only available on this menu if you have installed the DesignGuide Developer Studio. It brings up the DesignGuide Developer Studio documentation. Another way to access the Developer Studio documentation is by selecting *Help > Topics and Index > DesignGuides > DesignGuide Developer Studio* (from any ADS program window).

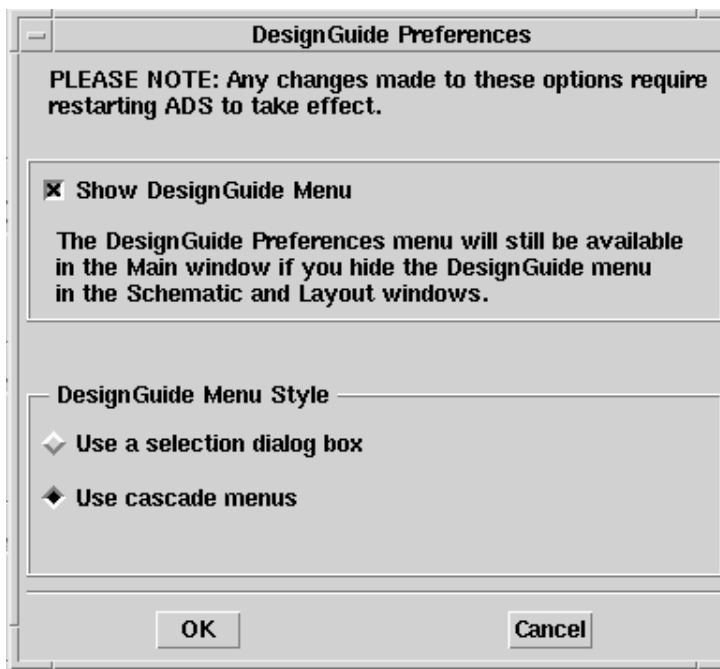
**DesignGuide Developer Studio > Start DesignGuide Studio** is only available on this menu if you have installed the DesignGuide Developer Studio. It launches the initial Developer Studio dialog box.

**Add DesignGuide** brings up a directory browser in which you can add a DesignGuide to your installation. This is primarily intended for use with DesignGuides that are custom-built through the Developer Studio.

**List/Remove DesignGuide** brings up a list of your installed DesignGuides. Select any that you would like to uninstall and choose the *Remove* button.

**Preferences** brings up a dialog box that allows you to:

- Disable the DesignGuide menu commands (all except Preferences) in the Main window by unchecking this box. In the Schematic and Layout windows, the complete DesignGuide menu and all of its commands will be removed if this box is unchecked.
- Select your preferred interface method (cascading menus vs. dialog boxes).



Close and restart the program for your preference changes to take effect.

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**Note** On PC systems, Windows resource issues might limit the use of cascading menus. When multiple windows are open, your system could become destabilized. Thus the dialog box menu style might be best for these situations.

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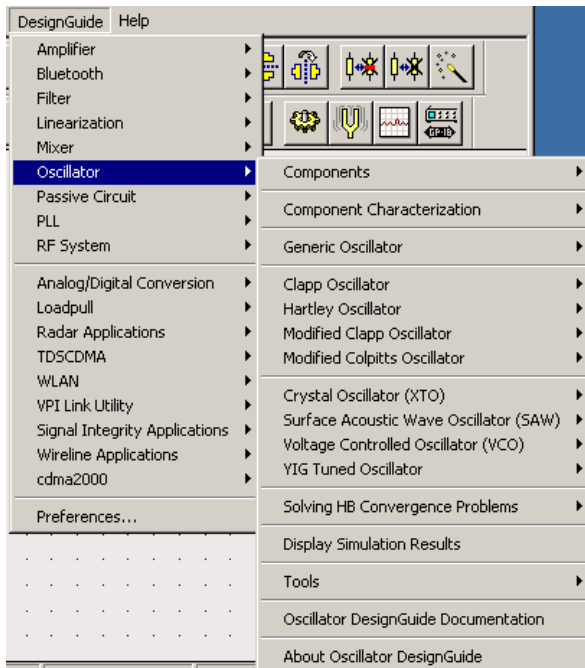
## Accessing the Documentation

To access the documentation for the DesignGuide, select either of the following:

- **DesignGuide > Oscillator > Oscillator DesignGuide Documentation** (from ADS Schematic window)
- **Help > Topics and Index > DesignGuides > Oscillator** (from any ADS program window)

## Basic Procedures

Access the Oscillator DesignGuide from the ADS Schematic window. Select *Design Guide > Oscillator Design Guide*, as shown here. All features of the Design Guide are available from the Oscillator DesignGuide menu.



The Guide contains the following:

- Nine oscillator circuits (Generic Oscillator, Clapp Oscillator, Hartley Oscillator, Modified Clapp Oscillator, Modified Colpitts Oscillator, XTO, SAW, VCO, and YTO), containing ready-to-use typical oscillator structures for fixed frequency (XTO, SAW) and tunable (VCO, YTO) oscillators in various frequency ranges.
- Component library (Components), providing useful building blocks for oscillator design.
- Selection of circuits that simulate their behavior (Component Characterization), providing simulations that characterize 1-ports and 2-ports.

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**Note** Selection of a component brings a component into a design. All other selections (oscillators and component characterization) bring a circuit and simulation into a design (replacing a previous design).

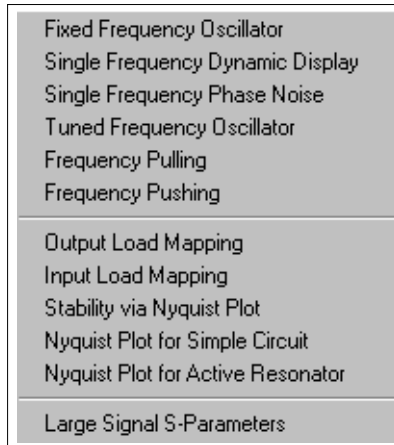
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## Component Sub-menu Structure

The Components menu contains a small custom library of resonators and devices, which can help in either modifying an existing oscillator or assembling a new one. They include: device DC and S-parameter characteristics and resonator and filter S-parameter and impedance/admittance characteristics

## Oscillator Sub-menu Structure

Select *DesignGuide*> *Oscillator DesignGuide* > *Generic Oscillator* and explore the entries in the cascading menu, as shown here.



Each Oscillator design is divided into two groups. large-signal measurements and linear/nonlinear design tools.

**Easy-to-use large-signal measurements** (for *push-button* nonlinear analysis), contain simulations of the following:

- Single-frequency oscillations
- Phase noise
- Tuned oscillations
- Frequency pulling
- Frequency pushing

This group is recommended as starting point for both an expert and a novice user. For the expert, it provides an overview of tool capabilities. For the novice user, it provides a working oscillator together with simulations of its typical characteristics. You can choose either the Generic Oscillator, Clapp Oscillator, Hartley Oscillator, Modified Clapp Oscillator, Modified Colpitts Oscillator, or one of the examples (XTO, SAW, VCO, YTO) to start a desired application.

**Linear and nonlinear design tools** include Output Load Mapping, Input Load Mapping, Stability via Nyquist Plot, Nyquist Plot for Simple Circuit, Nyquist Plot for Active Resonator, and Large Signal S-Parameters.

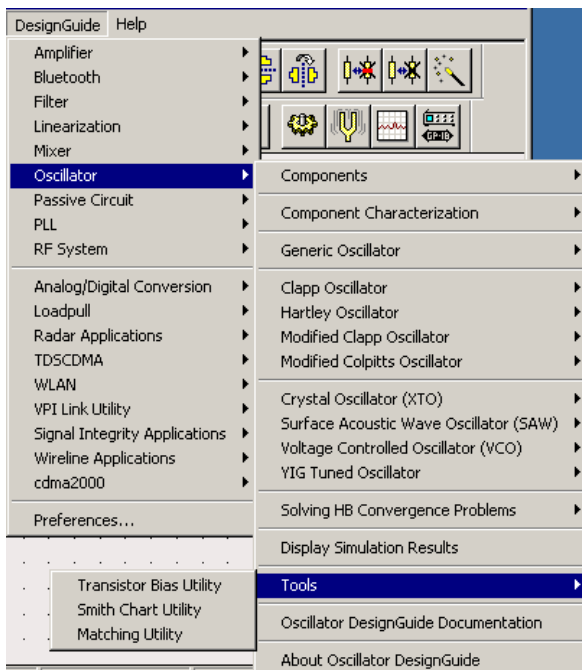
These tools are intended as an aide in designing an oscillator from scratch and in gaining insight into an existing oscillator. The full choice of tools is contained in the Generic Oscillator. The examples use only those tools that are useful in their particular case. The full set of tools include the following:

- Load mapping for load-to-resonator
- Resonator-to-load
- Nyquist stability criterion for varying  $Z_o$
- Two additional examples (only in Generic Oscillator), explaining the role of  $Z_o$

For nonlinear designs, large-signal S-parameters are defined and applied to oscillator power and frequency prediction.

## Tools Sub-menu Structure

These utilities provide added functionality to this DesignGuide. A brief description is provided for each. For more information select the *help* button located in each utility.



### Transistor Bias Utility

The Transistor Bias Utility provides *SmartComponents* and automated-assistants for the design and simulation of common resistive and active transistor bias networks. The automated capabilities can determine the transistor DC parameters, design an appropriate network to achieve a given bias point, and simulate and display the achieved performance. All *SmartComponents* can be modified when selected. You simply select a *SmartComponent* and with little effort redesign or verify their performance.

## **Smith Chart Utility**

This DesignGuide Utility provides full smith chart capabilities, synthesis of matching networks, allowing impedance matching and plotting of constant *Gain/Q/VSWR/Noise* circles. This guide assumes you have installed the associated DesignGuide with appropriate licensing codewords.

## **Impedance Matching Utility**

The Impedance Matching Utility performs the synthesis of lumped and distributed impedance matching networks based on provided specifications. The Utility features automatic simulation, sensitivity analysis, and display setup to enable simple and efficient component verification.

## Design Flow Example

Following is a simple design flow example for a fixed frequency oscillator.

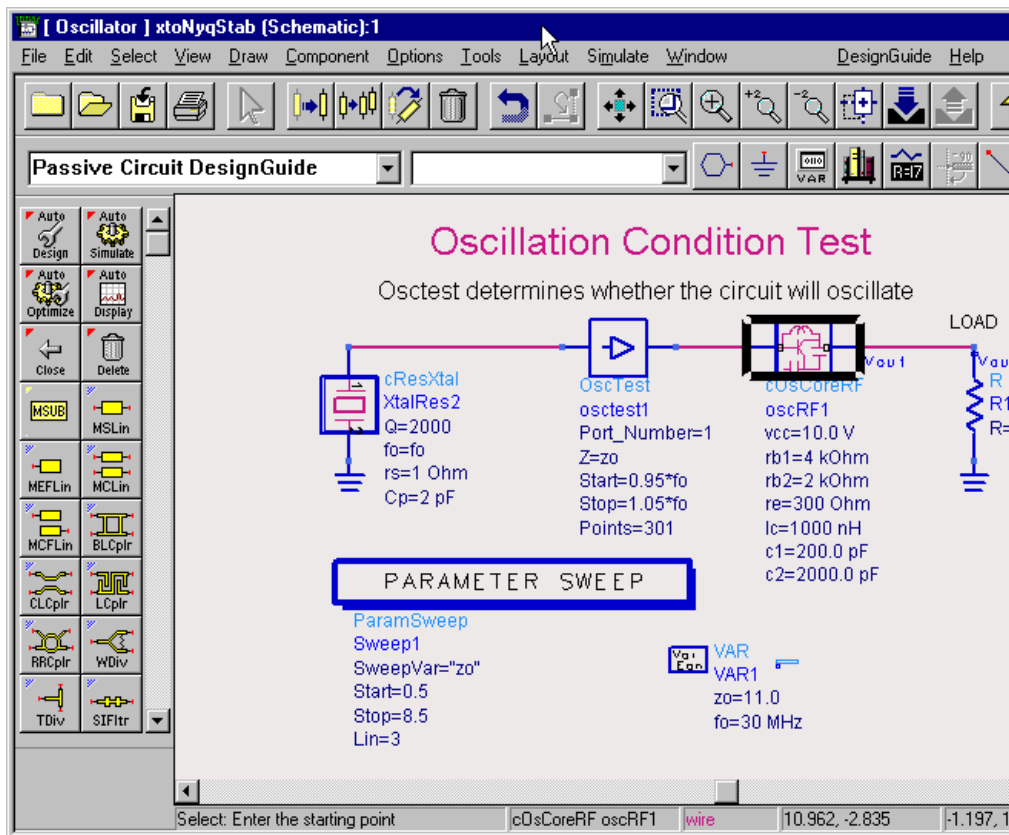
### Preliminary Steps

1. Open ADS.
2. Open a new or existing project.
3. Open a new Schematic window.
4. Select **DesignGuide > Oscillator DesignGuide**.
5. Select **Crystal Oscillator (XTO) > Stability via Nyquist Plot**.

## Important Preliminary Decisions

The schematic hides the following important choices:

- Device (a BJT)
- Biasing circuit
- Feedback scheme (Colpitts used in OsCore)
- Biasing point, which is shown in the OsCore subcircuit (which you can see from the Schematic window by clicking the **OsCore** component in the design, then pressing the down-arrow from the Main menu). The schematic is shown in the following illustration.



Moreover, the 30 MHz resonance frequency is assumed and the 20-ohm load resistance that models the actual load seen through a buffer amplifier and a matching circuit. At this point, you might want to modify the resonator and the OsCore (or replace them by your own circuit).

The modified circuit can be saved as a new design. We want the S11 trace shown on the polar plot in the data display window to encircle the point  $1+j*0$ . If this does not happen, the circuit must be modified. In that case, the menu selections *Output Load Mapping* and *Input Load Mapping* will help in determining the circuit and the load matching. Refer to the [Chapter 2, Oscillator DesignGuide Reference](#) for details.

## Oscillator Performance

The following menu selections determine the oscillator performance:

- Fixed Frequency Oscillator
- Single Frequency Phase Noise
- Frequency Pulling
- Frequency Pushing

They determine the oscillation frequency and power, phase and amplitude noise, and circuit elements that contribute most to noise.

You can find frequency variations with load and bias. Modify (or replace) the subcircuits (the resonator, the OsCore, and the load resistance).



# Chapter 2: Oscillator DesignGuide Reference

This manual provides reference information on the use of the Oscillator DesignGuide.

## Oscillator Design Guide Structure

The Oscillator DesignGuide is integrated into Agilent EEsof's Advanced Design System environment, working as a smart library and interactive handbook for the creation of useful designs. It allows you to quickly design oscillators, interactively characterize their components, and receive in-depth insight into their operation. It is easily modifiable to user-defined configurations. The first release of this DesignGuide focuses on RF printed circuit boards and microwave oscillations.

In addition to the requirements of the ADS software, the Oscillator DesignGuide requires approximately 30 MBytes of additional storage space.

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**Note** This manual assumes that you are familiar with all of the basic ADS program operations. For additional information, refer to the *Schematic Capture and Layout* manual.

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The Oscillator DesignGuide contains templates that can be used in the ADS software environment. It consists of generic colpitts, clapp, modified colpitts, modified clapp, and hartley oscillator design examples, and a library of components and component characterization tools.

To assist both expert and novice oscillator developers in creating designs of various complexity, each example design is divided into three groups:

- Quick and simple push-button nonlinear oscillator measurements
- Easy-to-use design tools for small- and large-signal designs
- Customized library of components and component characterization tools

To access these groups, select *DesignGuide > Oscillator DesignGuide* from the ADS Schematic window, then select the appropriate examples and tools.

## Push-Button Nonlinear Measurements

The push-button nonlinear measurements are recommended as a starting point for both expert and novice users creating large-signal designs. For the expert, these measurements provide an overview of tool capabilities. For the novice user, they provide a working oscillator together with simulations of its typical characteristics of nonlinear designs. The full set of available large-signal measurements in the Generic Oscillator example are described in [Table 2-1](#). Subsets of these measurements appear in other examples. Refer to the section [“Additional Examples” on page 2-40](#).

## Descriptions of Push-Button Measurements

[Table 2-1](#) provides descriptions of the available push-button large-signal measurements, as well as the associated filenames for schematics and data displays.

Table 2-1. Push-Button Large-Signal Measurements

Measurement	Schematic Filenames	Data Display Filename
Fixed Frequency Oscillator  <b>Description:</b> Oscport simulation. Display shows Spectrum and waveforms for the output and resonator voltages, plus supplied DC power and the output power.	FixedFreqOsc.dsn ClappFixedFreqOsc.dsn HartleyFixedFreqOsc.dsn ModifiedClappFixedFreqOsc.dsn ModifiedColpittsFixedFreqOsc.dsn	FixedFreqOsc.dds ClappFixedFreqOsc.dds HartleyFixedFreqOsc.dds ModifiedClappFixedFreqOsc.dds ModifiedColpittsFixedFreqOsc.dds
Single Frequency Dynamics Display  <b>Description:</b> Only a display. Combines periodic waveforms from FixedFreqOsc.dsn with DC characteristics from czBJTCurveTracer.dsn.	n/a	LargeSignalDynamics.dds
Single Frequency Phase Noise	PhaseNoise.dsn ClappPhaseNoise.dsn HartleyPhaseNoise.dsn ModifiedClappPhaseNoise.dsn ModifiedColpittsPhaseNoise.dsn	PhaseNoise.dds ClappPhaseNoise.dds HartleyPhaseNoise.dds ModifiedClappPhaseNoise.dds ModifiedColpittsPhaseNoise.dds

**Table 2-1. Push-Button Large-Signal Measurements**

<b>Measurement</b>	<b>Schematic Filenames</b>	<b>Data Display Filename</b>
Tuned Frequency Oscillator	FreqTune.dsn ClappFreqTune.dsn HartleyFreqTune.dsn ModifiedClappFreqTune.dsn ModifiedColpittsFreqTune.dsn	FreqTune.dds ClappFreqTune.dds HartleyFreqTune.dds ModifiedClappFreqTune.dds ModifiedColpittsFreqTune.dds
Frequency Pulling	FreqPull.dsn ClappFreqPull.dsn HartleyFreqPull.dsn ModifiedClappFreqPull.dsn ModifiedColpittsFreqPull.dsn	FreqPull.dds ClappFreqPull.dds HartleyFreqPull.dds ModifiedClappFreqPull.dds ModifiedColpittsFreqPull.dds
Frequency Pushing	FreqPush.dsn ClappFreqPush.dsn HartleyFreqPush.dsn ModifiedClappFreqPush.dsn ModifiedColpittsFreqPush.dsn	FreqPush.dds ClappFreqPush.dds HartleyFreqPush.dds ModifiedClappFreqPush.dds ModifiedColpittsFreqPush.dds

## Linear and Nonlinear Design Tools

The linear and nonlinear design tools are intended to facilitate you in designing an oscillator from scratch and in gaining insight into an existing oscillator. The full selection of tools is contained in the Generic Oscillator example. Other examples use only those tools that are useful in their particular case.

## Descriptions of Design Tools

Table 2-2 provides descriptions of the available design tools.

Table 2-2. Design Tools

Measurement	Schematic Filename	Data Display Filename
Output Load Mapping  <b>Description:</b> Mapping of passive loads onto resonator interface plane	MapLoad.dsn ClappMapLoad.dsn HartleyMapLoad.dsn ModifiedClappMapLoad.dsn ModifiedColpittsMapLoad.dsn	MapLoad.dds ClappMapLoad.dds HartleyMapLoad.dds ModifiedClappMapLoad.dds ModifiedColpittsMapLoad.dds
Input Load Mapping  <b>Description:</b> Mapping of passive loads from resonator interface plane to output plane	MapInput.dsn ClappMapInput.dsn HartleyMapInput.dsn ModifiedClappMapInput.dsn ModifiedColpittsMapInput.dsn	MapInput.dds ClappMapInput.dds HartleyMapInput.dds ModifiedClappMapInput.dds ModifiedColpittsMapInput.dds
Stability via Nyquist Plot  <b>Description:</b> Tests whether the circuit will oscillate (using OscTest to plot Nyquist loop)	NyqStab.dsn ClappNyqStab.dsn HartleyNyqStab.dsn ModifiedClappNyqStab.dsn ModifiedColpittsNyqStab.dsn	NyqStab.dds ClappNyqStab.dds HartleyNyqStab.dds ModifiedClappNyqStab.dds ModifiedColpittsNyqStab.dds
Nyquist Plot for Simple Circuit  <b>Description:</b> Simple example that explains effect of $z_o$ in OscTest use	NyqPlot.dsn	NyqPlot.dds

Table 2-2. Design Tools

Measurement	Schematic Filename	Data Display Filename
<p>NyqPlot for Active Resonator</p> <p><b>Description:</b> Simple example that further explains intricacies of Nyquist plots and the use of OscTest</p>	NyqPlotA.dsn	NyqPlotA.dds
<p>LSSpar (nonlinear tool)</p> <p><b>Description:</b> Image of Kurokawa Plots, their justification (describing function method), and definition of large signal S-parameters. Tools for easy selection of Kurokawa Plots (presented in KurPlot1)</p>	<p>LSSpar.dsn ClappLSSpar.dsn HartleyLSSpar.dsn ModifiedClappLSSpar.dsn ModifiedColpittsLSSpar.dsn</p>	<p>LSSpar.dds ClappLSSpar.dds HartleyLSSpar.dds ModifiedClappLSSpar.dds ModifiedColpittsLSSpar.dds</p>

## Components and Component Characterization Tools

The items in the Components and Component Characterization libraries contain a small custom library of resonators and devices, which can help in either modifying an existing oscillator or assembling a new one. They include device DC and S-parameter characteristics, as well as resonator and filter S-parameter and impedance/admittance characteristics.

[Table 2-3](#) through [Table 2-5](#) provide schematic filenames and brief descriptions for each component. [Table 2-6](#) provides schematic filenames and brief descriptions for each component characterization tool.

### Available Components

[Table 2-3](#) provides schematic filenames and descriptions for the available active device components.

Table 2-3. Available Active Device Components

Component Description	Schematic Filename	Description
Biased BJT	cBJTBiased.dsn	Common Emitter BJT with a standard (1-voltage source) biasing circuit
Biased RF BJT	cBJTRFBiased.dsn	The RF version of BJT used in Crystal Oscillator
Biased MESFET	cFETBiased.dsn	n/a
Varactor Diode	cVar.dsn	Varactor diode model, included for convenience. Within this DesignGuide, it always appears with the reverse-biasing circuit (see next table entry).
Biased Varactor Diode	cbVar.dsn	Reversed-biasing varactor

**Table 2-4** provides schematic filenames and descriptions for the available subcircuit components.

**Table 2-4. Available Subcircuit Components**

<b>Component Description</b>	<b>Schematic Filename</b>	<b>Description</b>
Buffer Amplifier (microwave)	cAmpBuff.dsn	A simple amplifier with capacitive feedback used in frequency pull and push simulations, used above 2GHz (for lower frequencies, see below). You are encouraged to replace it by your own amplifier and matching circuit.
Buffer Amplifier (1 - 2 MHz)	cAmpBuffS.dsn	Buffer amplifier with reactive components adjusted for 1GHz to 2GHz range, used in SAW oscillator
Buffer Amplifier (10 - 100 MHz)	cAmpBuffX.dsn	Buffer amplifier with reactive components adjusted for 10MHz to 100MHz range, used in crystal oscillator
Oscillator Core	cOsCore.dsn	Colpitts structure with a BJT with standard bias
RF Oscillator Core	cOsCoreRF.dsn	Oscillator Core adapted to MHz frequency range
Clapp Oscillator Core	cClappCore.dsn	Bipolar Clapp Oscillator covering a frequency range of 0.5 to 15GHz. (Other frequencies are possible by modifying component values.)
Hartley Oscillator Core	cHartleyCore.dsn	Bipolar Hartley Oscillator covering a frequency range of 1 to 1000MHz (Other frequencies are possible by modifying component values.)
Modified Clapp Oscillator Core	cModifiedClappCore	Bipolar Modified Clapp Oscillator covering a frequency range of 0.7 to 7.2GHz. (Other frequencies are possible by modifying component values.)
Modified Colpitts Oscillator Core	cModifiedCopittsCore	Bipolar Colpitts Oscillator covering a frequency range of 0.8 to 6.5GHz. (Other frequencies are possible by modifying component values.)
Fixed VSWR Complex Load	cLoadEqs.dsn	Load determined through VSWR and phase of the reflection coefficient

## Oscillator Core Circuits

Figure 2-1 shows the cClappCore.dsn oscillator core schematic symbol.

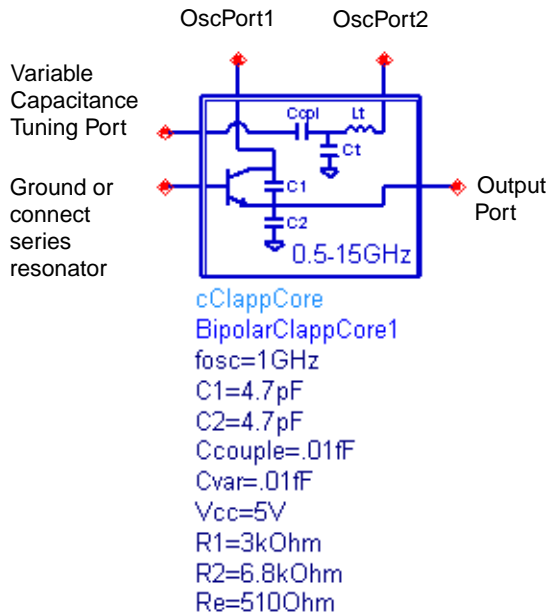


Figure 2-1. Bipolar Clapp Oscillator Schematic Symbol

The cClappCore.dsn oscillator operates from 0.5 to 15GHz using the existing component values in the Clapp oscillator sub-circuit. Resonator tank components  $C_t$  and  $L_t$  are automatically calculated with approximations referenced to 1GHz and displayed on some display pages. The *Ground or Series Resonator* port is either connected directly to ground or connected to ground through a series resonator. The *Variable Capacitance Tuning Port* is used for VCO design by coupling a varactor diode across the tank capacitor  $C_t$ . Coupling is accomplished by capacitor  $C_{cpl}$ . Larger values of  $C_{cpl}$  yield tighter coupling and wider tuning range for a given amount of tuning capacitance variation. The *OscPort1* and *OscPort2* ports are either connected directly together or connected through a series resonator. You can connect the *OscPort* test probe between these two ports for harmonic balance oscillator simulations. The *Output Port* is used for oscillator signal output. The variable  $C_{tune}$  sets the oscillator at the desired oscillation frequency when the capacitance across the *Variable Capacitance Tuning Port* is equal to the  $C_{tune}$  set value. Figure 2-2 is an example of the Clapp Oscillator subcircuit.

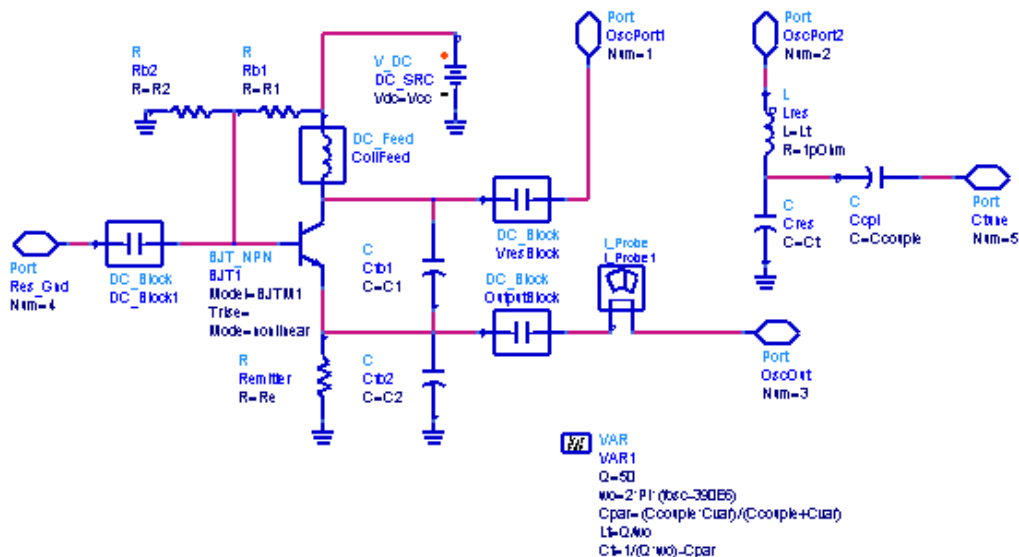


Figure 2-2. Bipolar Clapp Oscillator Subcircuit

Increase capacitors  $C1$  and  $C2$  for lower frequency oscillator circuits. Adjust resonator frequency offset (currently 390E6) to recenter oscillator frequency.

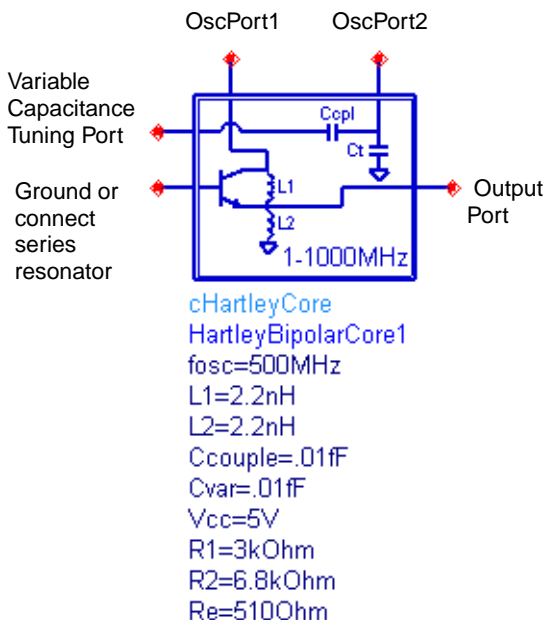


Figure 2-3. Bipolar Hartley Oscillator Schematic Symbol

Figure 2-3 shows the *cHartleyCore.dsn* oscillator core schematic symbol. This oscillator operates from 1 to 1000MHz using the existing component values in the Hartley oscillator sub-circuit. The resonator tank component  $C_t$  is scaled from 500MHz with an approximation and displayed on some display pages. You can connect the *Ground or Series Resonator* to the port directly to ground, or it can be connected to ground through a series resonator. The *Variable Capacitance Tuning Port* is used for VCO design by coupling a varactor diode across the tank capacitor  $C_t$ . Coupling is accomplished by capacitor  $C_{cpl}$ . Larger values of  $C_{cpl}$  yield tighter coupling and wider tuning range for a given amount of tuning capacitance variation. The *OscPort1* and *OscPort2* ports are either connected directly together or connected through a series resonator. For harmonic balance oscillator simulations, connect the *OscPort* test probe between these two ports. The *Output Port* is used for oscillator signal output. The variable  $C_{tune}$  sets the oscillator at the desired oscillation frequency when the capacitance across the *Variable Capacitance Tuning Port* is equal to the  $C_{tune}$  set value. Figure 2-4 shows the Hartley Oscillator subcircuit.

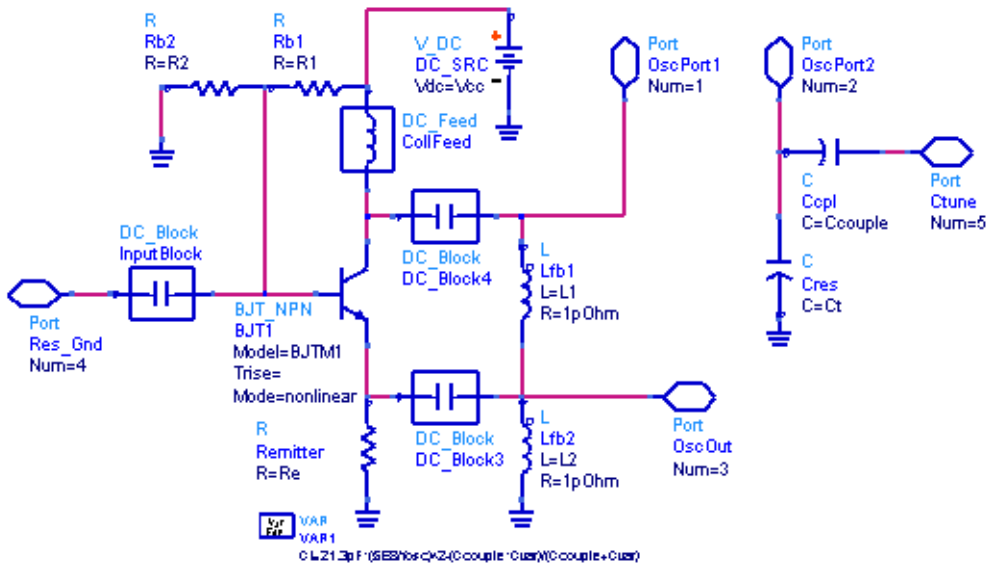


Figure 2-4. Bipolar Hartley Oscillator Subcircuit

Increase inductors  $L1$  and  $L2$  for lower frequency oscillator circuits. Adjust resonator capacitor  $Ct$  reference value (currently 21.3pF) to recenter oscillator frequency.

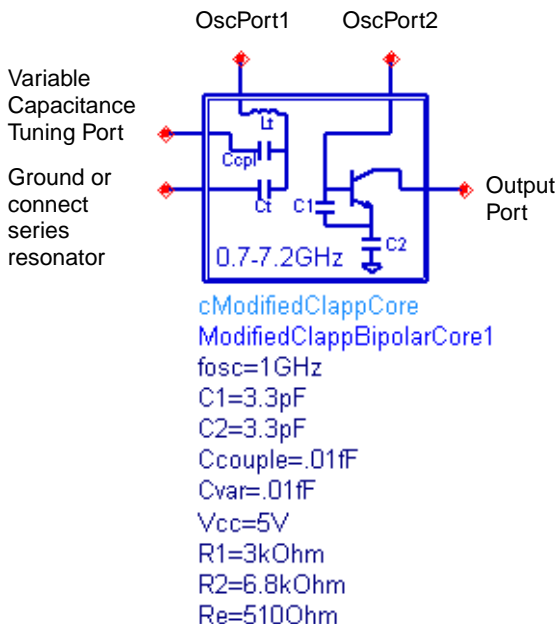


Figure 2-5. Bipolar Modified Clapp Oscillator Schematic Symbol

Figure 2-5 shows the `cModifiedClappCore.dsn` oscillator core schematic symbol. The `cModifiedClappCore.dsn` oscillator operates from 0.7 to 7.2GHz using the existing component values in the Modified Clapp oscillator sub-circuit. Resonator tank components  $C_t$  and  $L_t$  are automatically calculated with approximations referenced to 1GHz and displayed on some display pages. The *Ground or Series Resonator* port can be connected directly to ground or connected to ground through a series resonator. The *Variable Capacitance Tuning Port* is used for VCO design by coupling a varactor diode across the tank capacitor  $C_t$ . Coupling is accomplished by capacitor  $C_{cpl}$ . Larger values of  $C_{cpl}$  yield tighter coupling and wider tuning range for a given amount of tuning capacitance variation. The *OscPort1* and *OscPort2* ports are either connected directly together or connected through a series resonator. You can connect the *OscPort* test probe between these two ports for harmonic balance oscillator simulations. The *Output Port* is used for oscillator signal output. Variable *Ctune* sets the oscillator at the desired oscillation frequency when the capacitance across the *Variable Capacitance Tuning Port* is equal to the *Ctune* set value. Figure 2-6 shows the Modified Clapp Oscillator subcircuit.



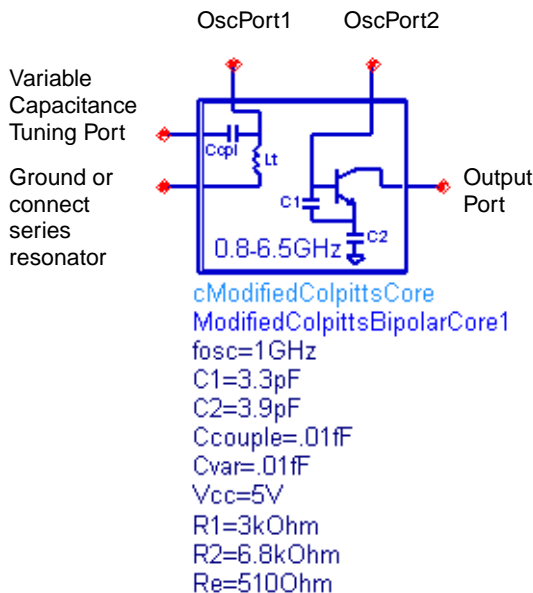


Figure 2-7. Bipolar Modified Colpitts Oscillator Schematic Symbol

Figure 2-7 shows the `cModifiedColpittsCore.dsn` oscillator core schematic symbol. This oscillator operates from 0.8 to 6.5GHz using the existing component values in the Modified Colpitts oscillator sub-circuit. Resonator tank inductor  $L_t$  is automatically calculated with an approximation and displayed on some display pages. The *Ground or Series Resonator* port can be connected directly to ground or connected to ground through a series resonator. The *Variable Capacitance Tuning Port* is used for VCO design by coupling a varactor diode across the tank inductor  $L_t$ . Coupling is accomplished by capacitor  $C_{cpl}$ . Larger values of  $C_{cpl}$  yield tighter coupling and wider tuning range for a given amount of tuning capacitance variation. The *OscPort1* and *OscPort2* ports are either connected directly together or connected through a series resonator. Connect the *OscPort* test probe between these two ports for harmonic balance oscillator simulations. The *Output Port* is used for oscillator signal output. The variable  $C_{tune}$  sets the oscillator at the desired oscillation frequency when the capacitance across the *Variable Capacitance Tuning Port* is equal to the  $C_{tune}$  set value. Figure 2-8 is the Modified Colpitts Oscillator subcircuit.

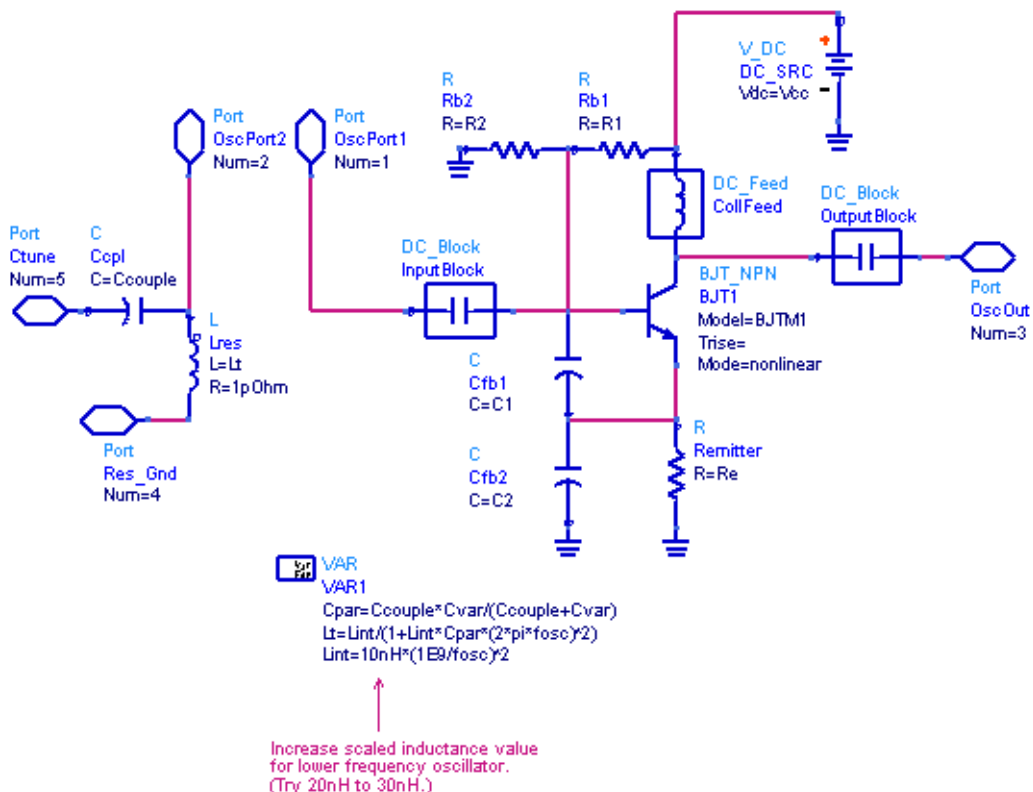


Figure 2-8. Bipolar Modified Colpitts Oscillator Subcircuit

Increase scaled 10nH inductance reference value for lower frequency oscillator circuits. Tank inductance  $L_t$  is scaled from 1GHz using an approximation. Increasing  $C_1$  and  $C_2$  capacitors yields a lower frequency oscillator as well.

Table 2-5 provides schematic filenames and descriptions for the available resonator components.

Table 2-5. Available Resonator Components

Component Description	Schematic Filename	Description
Crystal Resonator	cResXtal.dsn	Straightforward resonator model
SAW Resonator	cResSAW.dsn	Straightforward resonator model
YIG Resonator	cResYIG.dsn	Straightforward resonator model

Table 2-5. Available Resonator Components

Component Description	Schematic Filename	Description
Parallel Resonator	cResP.dsn	Straightforward resonator model
Series Resonator	cResS.dsn	Straightforward resonator model

## Available Component Characterization Tools

Table 2-6 provides schematic and data display filenames and descriptions for the component characterization tools.

Table 2-6. Available Component Characterization Tools

Tool Description	Schematic Filename	Data Display Filename	Description
S-parameters for 1-port	cz1PortSp.dsn	cz1PortSp.dds	S-parameter simulation of a 1-port
S-parameters for 2-port	cz2PortSp.dsn	cz2PortSp.dds	S-parameter simulation of a 2-port. Uses the Buffer Amplifier
BJT Curve Tracer	czBJTCurveTracer.dsn	czBJTCurveTracer.dds	DC Curves for a common emitter BJT, they can be observed independently or combined with periodic waveforms in LargeSignal-Dynamics.dds
RF BJT Curve Tracer	czBJTRFCurveTracer.dsn	czBJTRFCurveTracer.dds	The RF version of BJT used in Crystal Oscillator
FET Curve Tracer	cFETBiased.dsn czFETCurveTracer.dsn	czFETCurveTracer.dds	n/a
S-parameters for Biased BJT	czBJTSp.dsn	czBJTSp.dds	S-parameter simulation of biased BJT

**Table 2-6. Available Component Characterization Tools (continued)**

<b>Tool Description</b>	<b>Schematic Filename</b>	<b>Data Display Filename</b>	<b>Description</b>
Capacitance and Admittance of Biased Varactor	czbVarSp.dsn	czbVarSp.dds	S-parameter simulation of the reversed biased varactor. Displays admittance values and capacitance versus the biasing voltage
S-parameters for Parallel Resonator	czResPSP.dsn	czResPSP.dds	n/a
S-parameters for Series Resonator	czResSSP.dsn	czResSSP.dds	n/a
S-parameters for Generic Resonator	czResScvSp.dsn	czResScvSp.dds	The resonator contains a series resonator with parallel capacitance.
S-parameters for Crystal Resonator	czResXtalSp.dsn	czResXtalSp.dds	n/a
S-parameters for SAW Resonator	czResSAWSp.dsn	czResSAWSp.dds	n/a
S-parameters for YIG Resonator	czResYIGSp.dsn	czResYIGSp.dds	n/a

## Generic Oscillator Example

The oscillator circuit for the Generic Oscillator example is set up as follows:

- Resonator
- Oscillator active part (OscCore)
- Load, which can include buffering amplifier and matching circuits

The tools consists of three parts, as explained in the section, [“Oscillator Design Guide Structure” on page 2-1](#).

For simplicity, we show the buffering amplifier in two designs only and don't include the matching circuits. The generic resonator is presented here by a series resonant circuit shunted by a capacitance, which can model either a tuning capacitance, or the effect of packaging.

The OsCore is the Colpitts structure, which was introduced soon after the invention of triode (called audion at the time). It is still widely used. It uses a capacitive RF transformer to provide feedback. The transformer capacitors together with the inductor determine the oscillation frequency  $f = 1/\sqrt{L \times C1 \times C2 / (C1 + C2)}$ .

The Load models impedance, as seen by the OsCore component. Typically, this is the input impedance of the buffering amplifier(s) (including matching circuits) and the actual load.

## Oscillator Simulation

The oscillator designs included in this DesignGuide provide easy access to observing the nonlinear behavior of an oscillator. The circuit design for the generic example is shown in [Figure 2-9](#). It includes fixed load, a clearly visible active circuit, and the tunable resonator separated by the *OscPort* component.

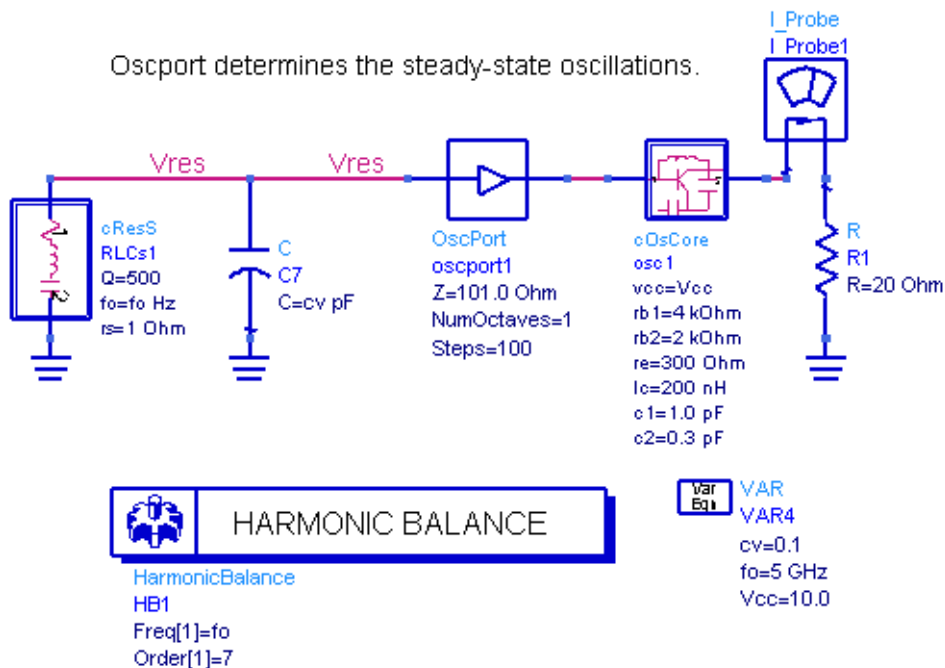


Figure 2-9. Structure of Oscillator Circuit

## Single-Frequency Oscillations

The results of single-frequency oscillations in [Figure 2-10](#) show output and resonator voltages. They also provide oscillations frequency, power harmonic content, the corresponding time-domain waveform, and the values of DC power and RF output power.

### FixedFreqOsc

Spectra and waveforms at oscillation frequency = 5.149 GHz

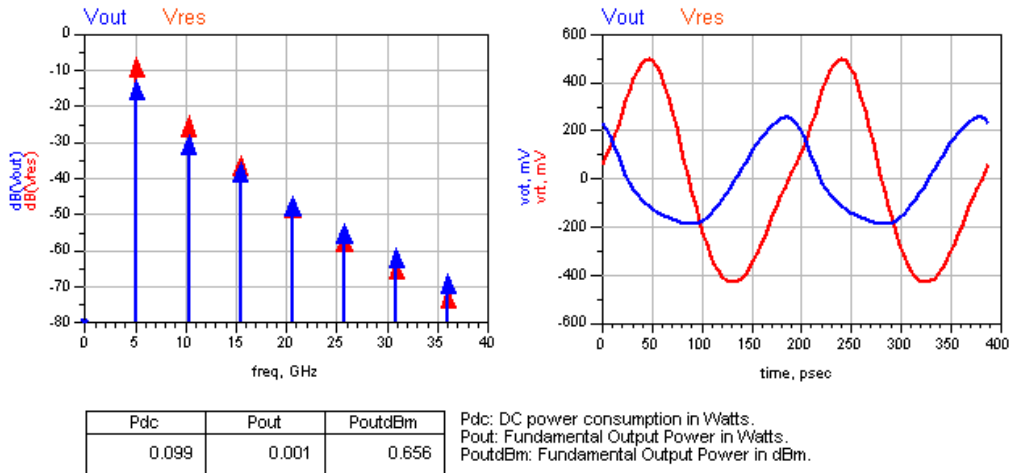


Figure 2-10. Results of Single-Frequency Oscillations

# Dynamics of Single-Frequency Oscillations

The graph in **Figure 2-11** shows waveforms of Collector-Emitter voltage and the collector current of the OsCore BJT superimposed on BJT's DC characteristics.

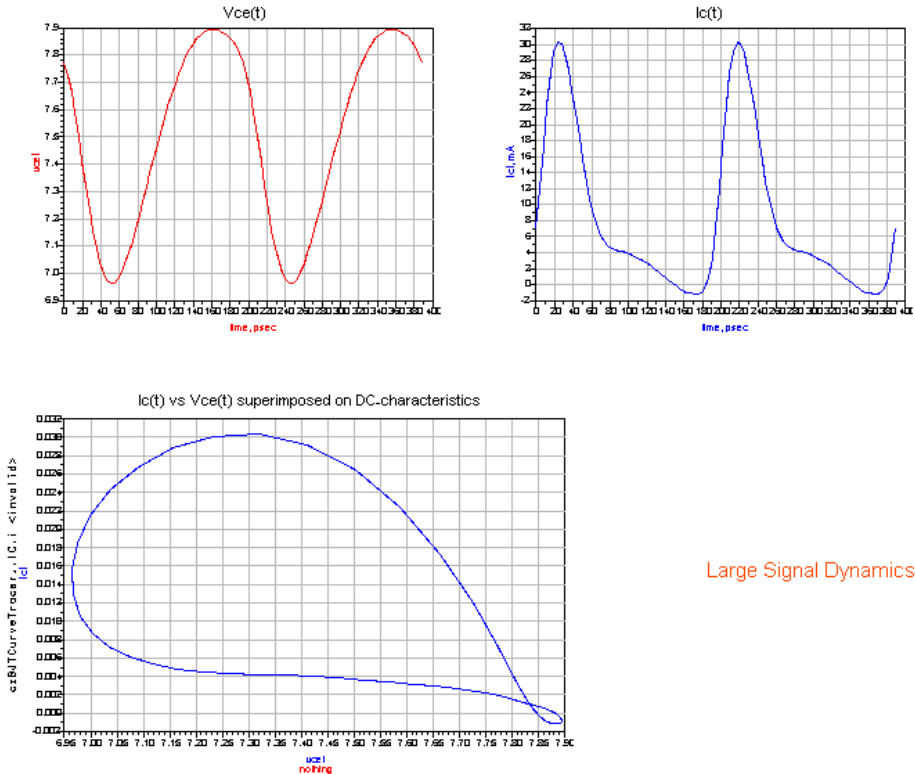


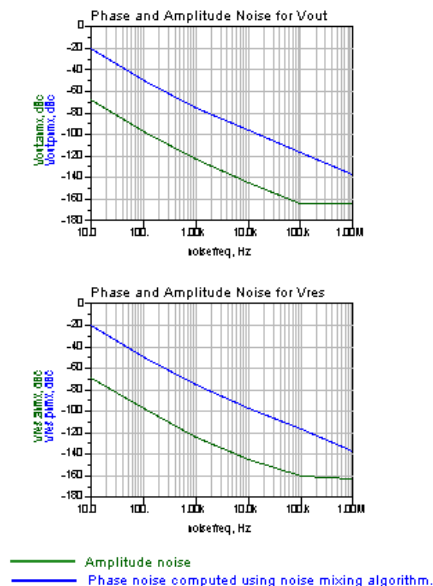
Figure 2-11. Dynamics of Single-Frequency Oscillation  
Figure 2-12.

## Single-Frequency Oscillations with Noise

The graph in [Figure 2-13](#) shows single-frequency results with the noise characteristics of Vout and Vres. It also lists the components that affect the noise the most. You can specify the range after which small contributors to noise will be neglected. In this example, the range is set to 15 dB.

Noise Contributions to Vout and Vres

Oscillation Frequency: 5.105 GHz  
Most significant contributors within 15.0 dB range.



index	Vout_NC.name	Vout_NC.vnc	Vres_NC.name	Vres_NC.vnc
noisefreq=10.0 Hz				
0	total	-19.8 dBc	total	-19.8 dBc
1	osc1.BJT1	-19.9 dBc	osc1.BJT1	-19.9 dBc
2	osc1.BJT1.flc...	-19.9 dBc	osc1.BJT1.flc...	-19.9 dBc
noisefreq=100.0 Hz				
0	total	-49.2 dBc	total	-49.2 dBc
1	osc1.BJT1	-49.5 dBc	osc1.BJT1	-49.5 dBc
2	osc1.BJT1.flc...	-50.0 dBc	osc1.BJT1.flc...	-50.0 dBc
3	osc1.BJT1.ice	-59.9 dBc	osc1.BJT1.ice	-59.9 dBc
4	R1	-62.2 dBc	R1	-62.3 dBc
noisefreq=1.00 kHz				
0	total	-75.1 dBc	total	-75.1 dBc
1	osc1.BJT1	-76.6 dBc	osc1.BJT1	-76.6 dBc
2	osc1.BJT1.ice	-79.9 dBc	osc1.BJT1.ice	-79.9 dBc
3	osc1.BJT1.flc...	-80.0 dBc	osc1.BJT1.flc...	-80.0 dBc
4	osc1.BJT1.Rb	-89.9 dBc	osc1.BJT1.Rb	-89.9 dBc
5	R1	-82.3 dBc	R1	-82.3 dBc
6	RLCs1.L1	-88.8 dBc	RLCs1.L1	-88.8 dBc
7	osc1.R8	-89.6 dBc	osc1.R8	-89.6 dBc
noisefreq=10.0 kHz				
0	total	-96.6 dBc	total	-96.6 dBc
1	osc1.BJT1	-98.9 dBc	osc1.BJT1	-98.9 dBc
2	osc1.BJT1.ice	-99.9 dBc	osc1.BJT1.ice	-99.9 dBc
3	osc1.BJT1.Rb	-110. dBc	osc1.BJT1.Rb	-110. dBc
4	osc1.BJT1.flc...	-110. dBc	osc1.BJT1.flc...	-110. dBc
5	R1	-102. dBc	R1	-102. dBc
6	RLCs1.L1	-109. dBc	RLCs1.L1	-109. dBc
7	osc1.R8	-110. dBc	osc1.R8	-110. dBc
noisefreq=100. kHz				
0	total	-117. dBc	total	-117. dBc
1	osc1.BJT1	-119. dBc	osc1.BJT1	-119. dBc
2	osc1.BJT1.ice	-120. dBc	osc1.BJT1.ice	-120. dBc
3	osc1.BJT1.Rb	-130. dBc	osc1.BJT1.Rb	-130. dBc
4	R1	-122. dBc	R1	-122. dBc
5	RLCs1.L1	-129. dBc	RLCs1.L1	-129. dBc
6	osc1.R8	-130. dBc	osc1.R8	-130. dBc
noisefreq=1.00 MHz				
0	total	-137. dBc	total	-137. dBc
1	osc1.BJT1	-139. dBc	osc1.BJT1	-139. dBc
2	osc1.BJT1.ice	-140. dBc	osc1.BJT1.ice	-140. dBc
3	osc1.BJT1.Rb	-150. dBc	osc1.BJT1.Rb	-150. dBc
4	R1	-142. dBc	R1	-142. dBc
5	RLCs1.L1	-149. dBc	RLCs1.L1	-149. dBc

Figure 2-13. Single-Frequency Oscillations with Noise

## Frequency Pulling

Figure 2-14 shows results after the original circuit is modified for frequency pull. Changes include the varying load and a simple buffer amplifier, which was added to make pulling values realistic. The load is specified in terms of VSWR. You can determine the best variation. The graph shows that frequency variation for varying phase of the load. VSWR is fixed, with its value shown above the plot. By moving the marker on the VSWR selection plot, you can obtain the results for other VSWR values. The corresponding load characteristic and the corresponding value of  $V_{out}$  fundamental are shown in the lower plots. The equation with pulling value will be added.

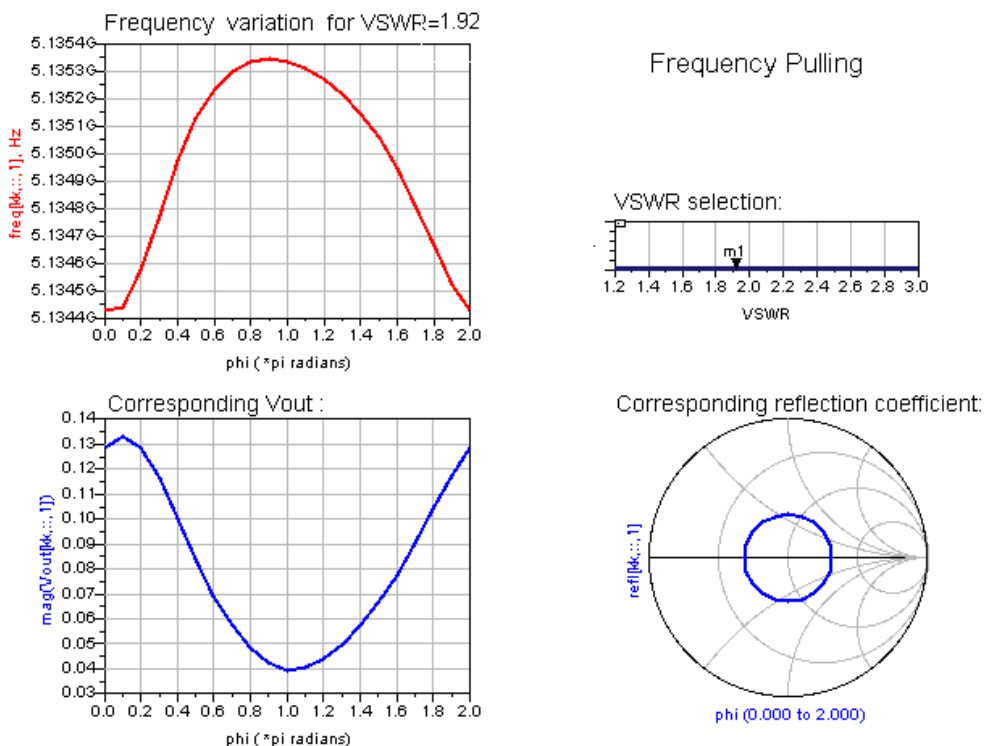


Figure 2-14. Results with Circuit Modified for Frequency Pulling

## Frequency Pushing

Figure 2-15 shows results after the frequency pull circuit is modified for frequency push, with fixed load ( $v_{swr}=1$ ,  $\phi_i=0$ ) and varying bias on oscillator's transistor  $V_{cc}$ . The display presents frequency variation with  $V_{cc}$ .

For  $V_{cc}=8V$ , the circuit does not oscillate, which results in the error message. Nevertheless, the sweep is performed, showing oscillations for higher bias. Two markers on the plot allow us to zoom in at the frequency plot. The plot to the right is determined by markers position. The corresponding value of  $V_{out}$  fundamental is shown in the lower plot.

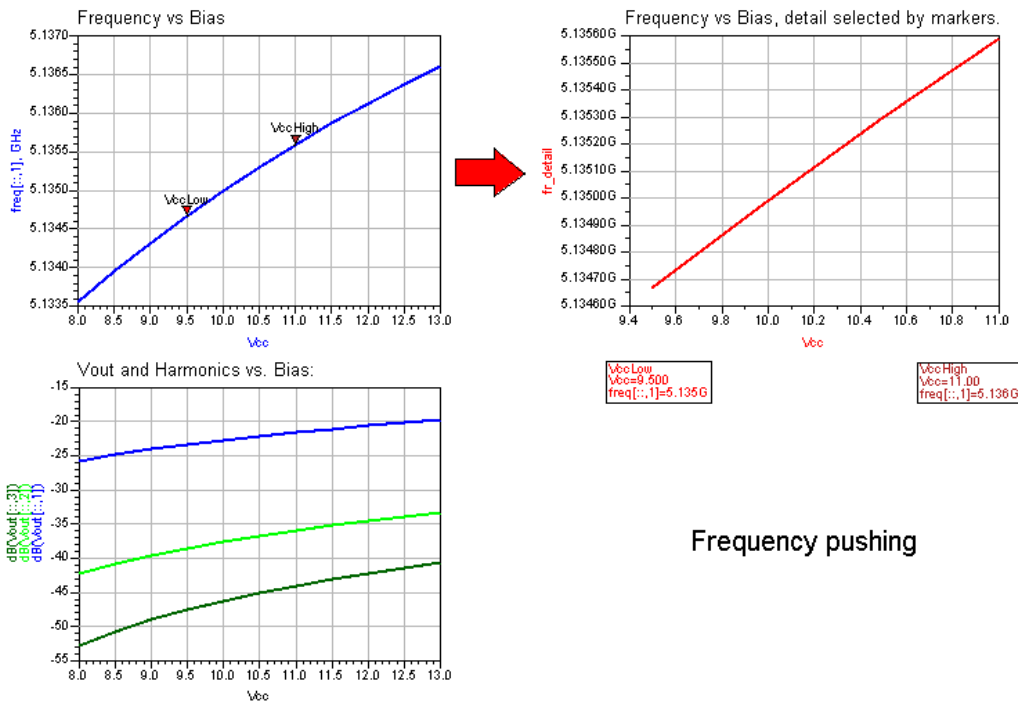


Figure 2-15. Results with Circuit Modified for Frequency Pushing

## Tuned Oscillations

Figure 2-16 shows the resonator voltage and its harmonics. It provides the tuning characteristics of sweeps vs. capacitance vs. frequency.

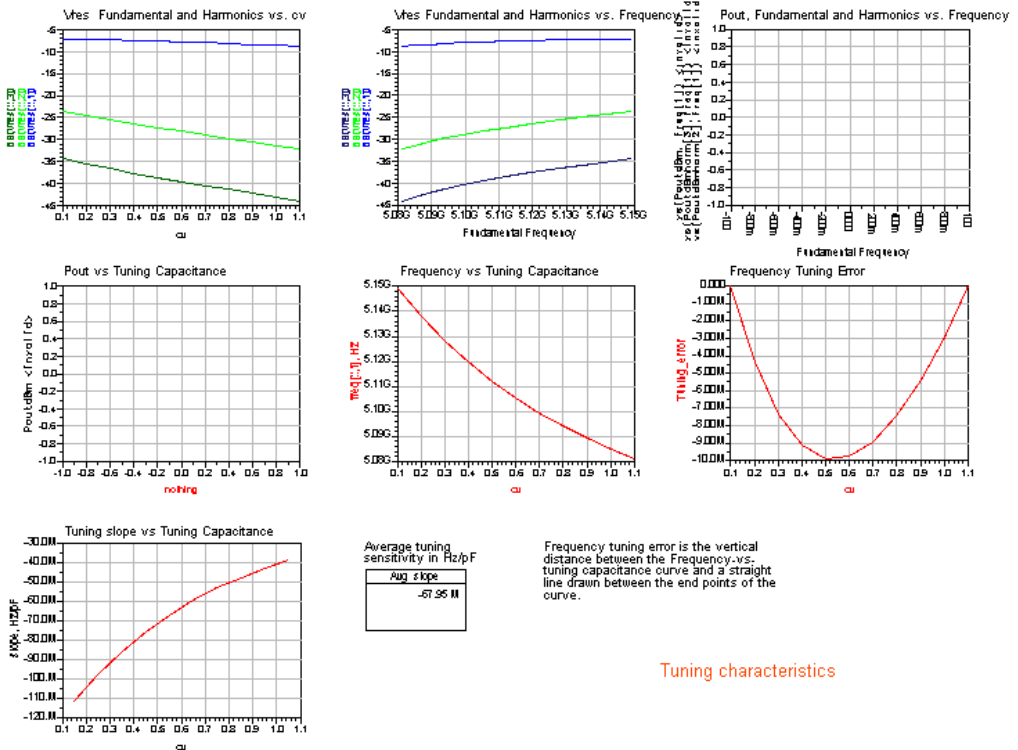


Figure 2-16. Resonator Voltage and Harmonics

## Linear Design Tools

The designs used for linear applications provide tools to investigate oscillations conditions. They belong to two groups, as follows:

- Necessary oscillation conditions
- Check for Nyquist stability criterion, using the linearized version of the OscPort component, called OscTest.

## Load-to-Resonator Mapping

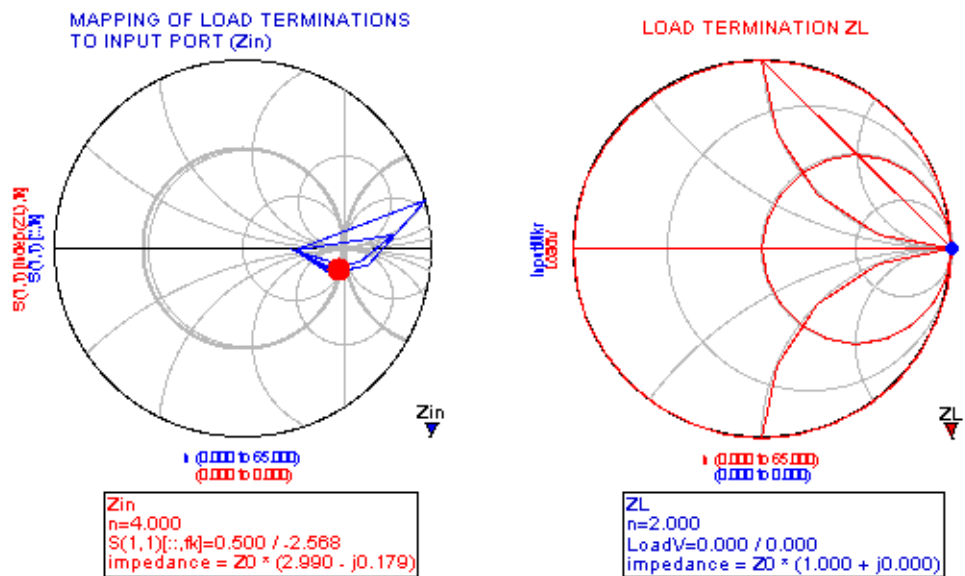
Load-to-resonator mapping is represented by the design *MapLoad.dsn* and the graph *MapLoad.dds*. The design consists of the oscillator circuit without the resonator. The buffer and load are replaced by varying load. The load values correspond to main traces on a Smith chart (shown in the *loadmap.dds* display). You can specify the number of samples per trace and the radius of the small circle. On the resonator side, the circuit is terminated by an S-parameter port. S-parameter analysis is performed over frequency band determined that you specify so that the mapping can be analyzed at various frequencies.

The purpose of the analysis is to observe how the different values of the load will be detected by the resonator. The values that map outside the unit circle are of particular interest. These are the values that will provide negative resistance facing the resonator, so that the necessary oscillations conditions will be satisfied

The graph *MapLoad.dds*, as shown in [Figure 2-17](#), represents load values and their image in the resonator plane. Color-coded markers facilitate orientation. The marker on the bottom plot selects frequency at which the mapping is performed.

## SMALL-SIGNAL LOAD MAPPING

Move markers 'Zin' (blue) or 'ZL' (red) to view the corresponding load point on the other trace.



Marker 'FRQ' selects the frequency at which the mapping is performed.

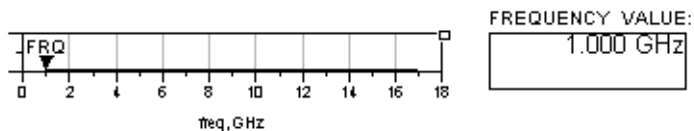
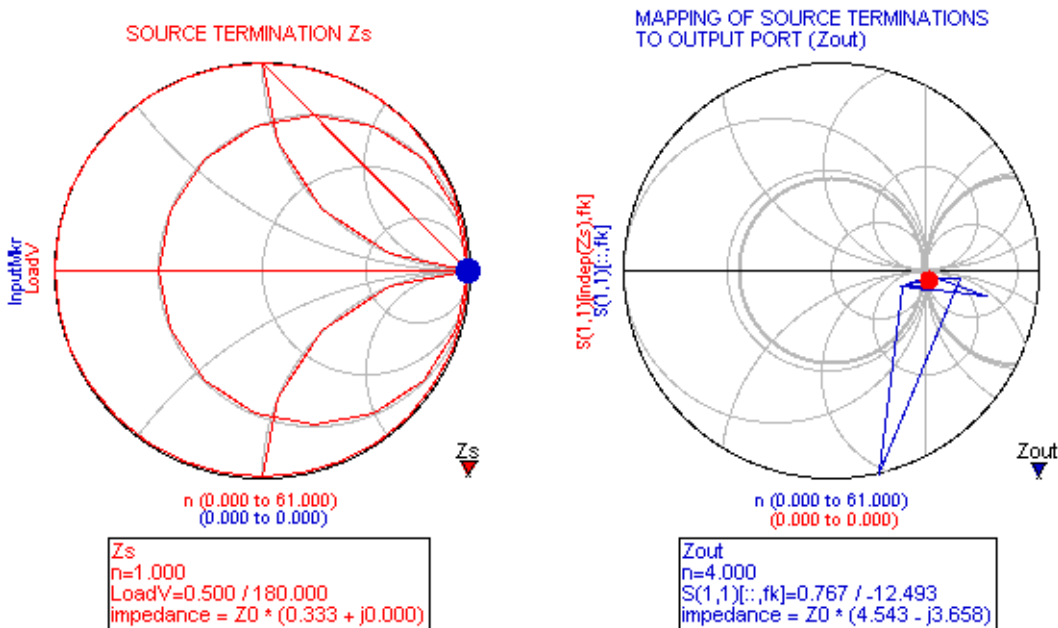


Figure 2-17. Load Values and Their Image in Resonator Plane

## Resonator-to-Load Mapping

Resonator-to-load mapping is represented by the design *MapInput.dsn* and the graph *MapInput.dds*, as shown in [Figure 2-18](#). The design is dual to load-to-resonator mapping, and it determines the image of the resonator at the buffer amplifier input. Consequently, it is useful in designing of the amplifier matching circuit. A special case restricted to a unit circle input gave rise to the method of stability circles (see Reference 1 in the section “Bibliography” on page 2-42).

**Move markers 'Zs' (red) or 'Zout' (blue) to view the corresponding load point on the other trace.**



Marker 'FRQ' selects the frequency at which the mapping is performed.

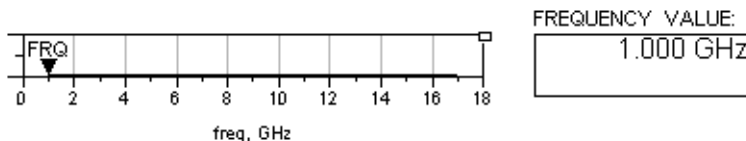
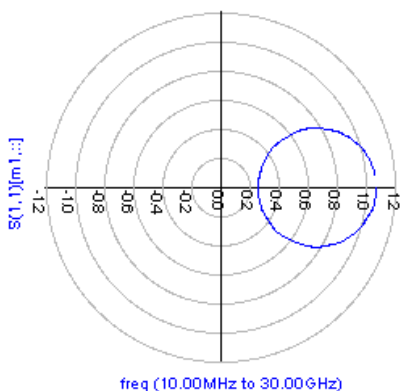


Figure 2-18. Resonator-to-Load Mapping

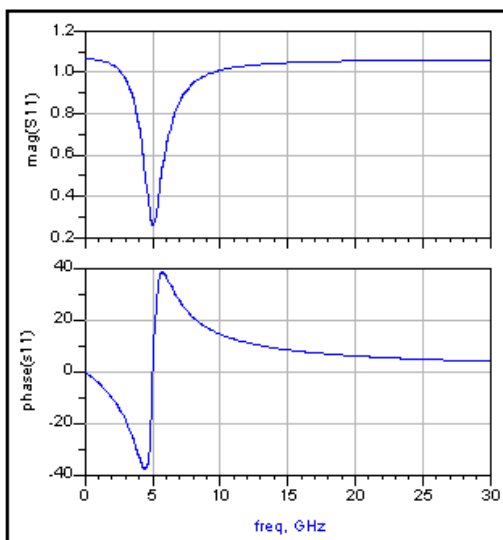
## Stability Via Nyquist Plots

The design Nyquist Stab shows the use of the OscTest component. The results shown for different choices of the OscTest characteristic impedance  $Z_0$  illustrate the importance of the Nyquist plot.  $Z_0$  is swept from 1.5ohm to 21.5 ohm in 10 steps. The plots clearly show that it is the encirclement of  $1+j0$  that matters (as we know from the Nyquist theorem) and not the value of S11 at the crossing of the real axis. The justification of this statement is illustrated by two simple designs (NyqStab, NyqStabA) described in the next section.

The circuit oscillates if the Nyquist loop (S11) encircles the point  $1+j*0$ , moving in the clockwise direction with increasing frequency.



The value of  $z_0$  changes the shape of the Nyquist plot but the number of encirclements remains the same.



Choice of  $z_0$ :

$z_0[m1]$
162.000

Select  $z_0$  by moving marker m1:

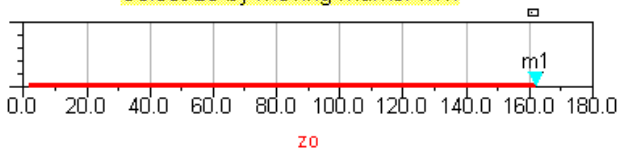


Figure 2-19. Resonator-to-Load Mapping

## Theory of Stability vs. Nyquist Plots

There is a widespread belief [3,7,9,10,11,12,13,14,15] that the stability of oscillators can be determined by a particular criterion. When the phase of the transfer function is zero and the magnitude (at the same frequency) is larger than one, the system is unstable. The circuit shown in the criterion is usually presented as two equations:

$$\arg(S_n) = -\arg(S_r), \quad |S_n S_r| > 1$$

Consider a simplest possible linearized oscillator, as shown in [Figure 2-20](#). The circuit has the resonator's resistance  $rr=1/G = 100.0$  ohm and the active (linearized) resistance  $ra = 1/g'(Vo) = -5.0$  ohm and is obviously unstable.

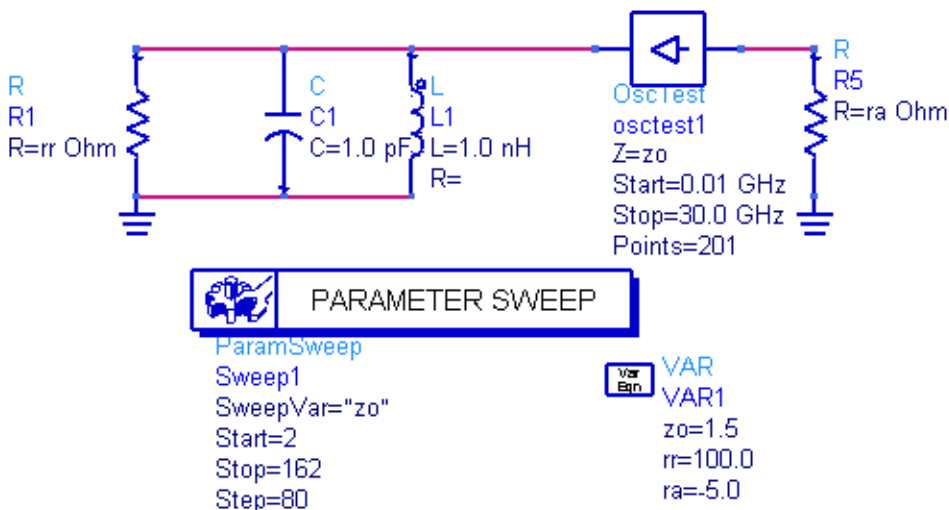
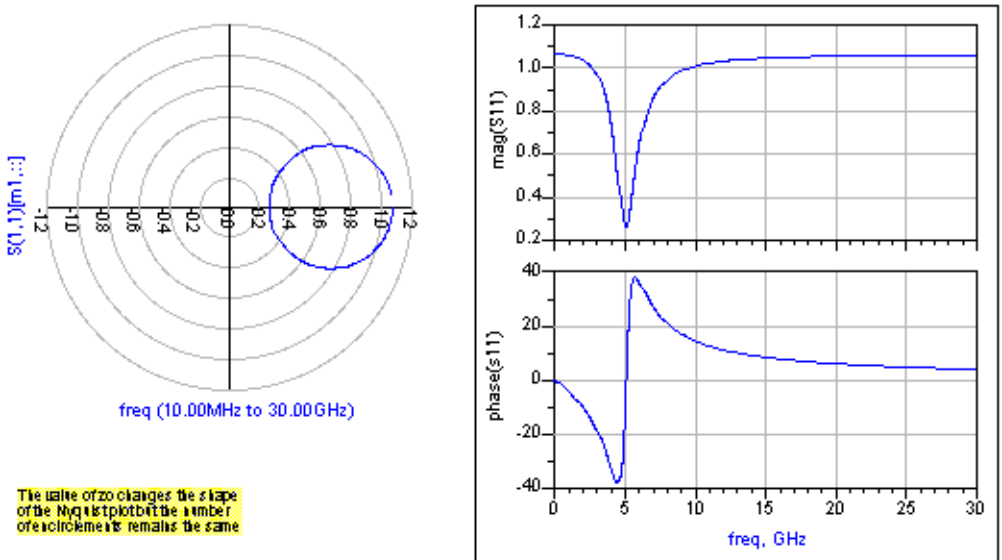


Figure 2-20. Stability Via Nyquist Plots

The Nyquist plots and equation were checked for different values of the characteristic impedance  $Z_o$ . The system stability depends on the position of poles of the transfer function  $S_r(s)S_n(s)$ . If the function possesses poles in the right half plane, the system is unstable. It follows from the Nyquist criterion that the presence of poles in the right-half plane and the system instability can be determined by the encirclements of point  $1+j0$  by the osctest generated contour  $S_{11} = S_r(j\omega)S_n(j\omega)$ .

The Nyquist plots obtained for  $Z_o = 2.0, 82.0, 162.0$  ohm are shown in [Figure 2-21](#).

The circuit oscillates if the Nyquist loop (S11) encircles the point  $1+j*0$ , moving in the clockwise direction with increasing frequency.



The value of  $z_0$  changes the shape of the Nyquist plot but the number of encirclements remains the same.

Choice of  $z_0$ :

$z_0[m1]$
162.000

Select  $z_0$  by moving marker m1:

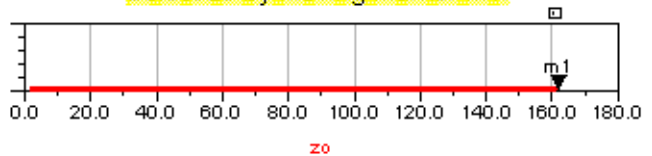


Figure 2-21. Nyquist Plot for Simple Linearized Oscillator

The three values of  $Z_0$  are chosen so that we get respectively:

$$Z_0 < |ra| < rr; \quad |ra| < Z_0 < rr$$

and

$$|ra| < rr < Z_0$$

The circuit is obviously unstable. Consequently, the Nyquist loop, shown in the plots on the left in Figure 2-22, encircles the point  $1+j0$  for every value of  $Z_0$ . However, if we turn to the magnitude-phase plots (shown to the right), then the circuit instability will be hard to deduce. Finally the intuitive condition (1) ( $S_n S_r > 1$  for  $arg(S_n S_r) = 0$ ) obviously fails for  $Z_0 = 82.0$  and  $Z_0 = 162.0$  ohm.

The  $SrSn$  contours clearly show that it is the encirclement of  $1+j0$  that matters (as we know from Nyquist theorem) and not the value of  $SnSr$  at the crossing of the real axis.

The Nyquist criterion is most useful when the open loop system is stable. In that case, the stability of the feedback system is determined by the closing of the feedback loop. **Figure 2-22** shows a variation of our original circuit.

The circuit oscillates if the Nyquist loop (S11) encircles the point  $1+j^0$ , moving in the clockwise direction with increasing frequency.

The above is true provided that the open loop circuit has no poles in the right half-plane

Note that  $S(1,1)$ , viewed as an open loop transfer function, becomes unstable for  $z0 > |ra|$ . In that case the usefulness of the Nyquist criterion is diminished.

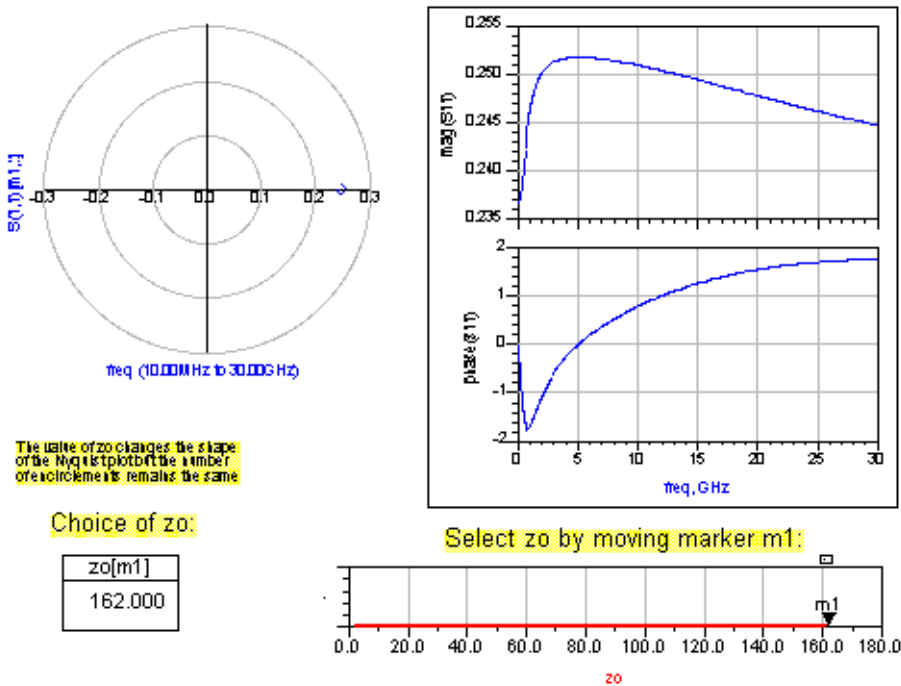


Figure 2-22. Nyquist Plot for Simple Circuit

In the circuit shown, the resistances were interchanged, resulting in an active resonator, for which adding the 100 ohms in parallel (i.e. loop closing) does not change its instability. Obviously, in this system, the Nyquist loop does not encircle the  $1+j0$ . This is because the open loop transfer function  $Y_n Z_r = (s/r_r C)/(s^2 + s/ra C + 1/LC)$  has two poles in the right half plane and loop closing does not add any new poles.

Therefore the position of the OscTest probe (which automatically computes  $S_r S_n$  in the simulator) should be carefully chosen. It should be placed between the resonator and the active circuit so that the open-loop system is stable.

The plots of S11 for  $Z_o = 2.0, 82.0, 162.0$  need to be considered. For  $Z_o = 82.0, 162.0$  the Nyquist loop does not encircle  $1+j0$ , as expected. However, for  $Z_o=2.0$  it does, which seems contrary to the fact that the resonator circuit is active. The explanation for this is that the open loop S-parameter transfer function:

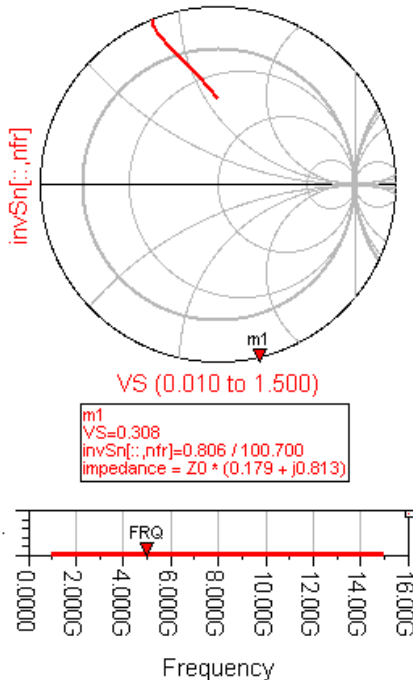
$$S_r(s) = (Z_r - Z_o)/(Z_r + Z_o) = -(s^2 + s((1/ra) - (1/Z_o))/C + 1/LC)/(s^2 + s((1/ra) + (1/Z_o))/C + 1/LC)$$

has all poles in the left hand plane for  $Z_o < |ra|$ . Only closing the loop makes the system unstable.

## Using Nonlinear Design Tools

### Kurokawa Plots

1/S<sub>n</sub> versus source amplitude, at one particular input frequency, selected by moving marker FRQ.



1/S<sub>n</sub> versus frequency, at one particular source amplitude, selected by moving marker V<sub>src</sub>.

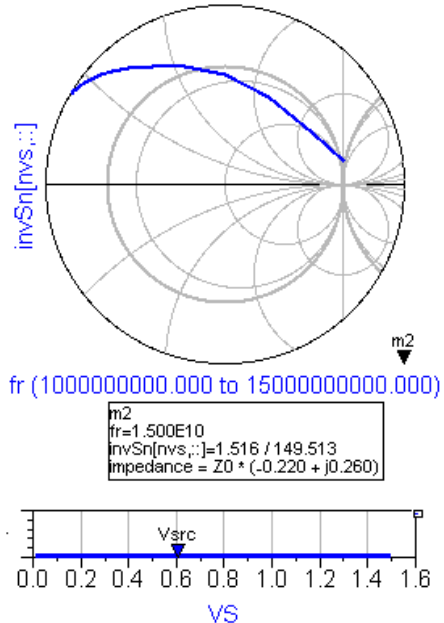


Figure 2-23. Large-Signal S-Parameter Design

In the steady-state, because of intrinsically nonlinear behavior of oscillators, signals are no longer sinusoidal. Consequently, the concepts of impedance and S-parameters are not obvious. However, in high-Q circuits, in which signals are represented by their fundamental components, there is a natural way to define the large-signal impedance, or large-signal S-parameters. In this section, we define the large signal S-parameters and demonstrate that the equations

$$\arg(S_n) = -\arg(S_r)$$

$$|S_n S_r| = 1$$

determine amplitude and phase of oscillations.

The steady state periodic oscillations can be represented by their Fourier series:

$$v(t) = \sum |vn| \cos(n\omega t + \varphi n) \quad i(t) = \sum |in| \cos(n\omega t + \psi n)$$

For a high-Q resonator the higher harmonics are negligibly small and voltage and current can be approximated by:

$$v(t) \approx |V| \cos(\omega t + \varphi)$$

$$i(t) \approx |I| \cos(\omega t + \psi)$$

where

$$V = |V| \exp(j\varphi), \text{ and } I = |I| \exp(j\psi)$$

denote the fundamental components of voltage and current.

Thus the signals are represented by their complex amplitudes V and I, for which we define the large-signal incident and reflected waves:

$$a = (V + Z_0 I) / (2\sqrt{Z_0}), \quad b = (V - Z_0 I) / (2\sqrt{Z_0})$$

On the resonator side, we have  $a = S r b$ , with  $b = b(a)$  on the active circuit side. These two relationships provide us with the steady-state equations  $a = S r b(a)$ . After defining the large signal S-parameter:  $S n = b(a) / a$  the steady state equation can be represented as  $a = S r S n a$ , which leads to:  $1 = S r S n$ , which is equivalent to the equations.

## Solving Harmonic Balance Convergence Problems

Harmonic balance simulation in Advanced Design System is an excellent way to analyze many oscillators in the frequency domain. Occasionally, you might have an oscillator that converges in a time-domain simulation, but the harmonic balance oscillator algorithm is unable to find the solution. There are two techniques in ADS for solving those oscillators:

- Analyzing the large-signal loop gain in harmonic balance to find the point of oscillation and using that as an initial guess for the full harmonic balance oscillator analysis
- Using a transient analysis to produce an initial guess for harmonic balance oscillator analysis.

In the DesignGuide > Oscillator menu, select *Solving Harmonic Balance Convergence Problems* for several useful design examples.

For more information on these techniques, refer to the ADS Circuit Simulation manual, in the section “Simulation Techniques for Recalcitrant Oscillators, in Chapter 9, “Harmonic Balance for Oscillator simulations.

## Oscillator Core Examples

Oscillator Cores (cClappCore, cHartleyCore, cModifiedClappCore, and cModifiedColpittsCore) are compatible with simulation and measurement setups outlined by the Generic Oscillator Example. These core oscillator circuits are configured for low resistance loads. (50ohm is used for these four oscillator cores, although other load values are possible.) The design and display filenames for these examples follow a naming convention that indicates oscillator type and simulation setup as follows:

- *OscillatorTypeSimulationType.dsn*
- *OscillatorTypeSimulationType.dds*

where:

*OscillatorType* indicates one of the following topologies: Clapp, Hartley, Modified Clapp, or Modified Colpitts.

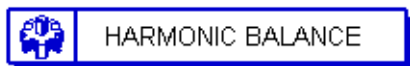
*SimulationType* provides one of the following simulation set-ups or measurements: FixedFreqOsc, FreqPull, FreqPush, FreqTune, LSSpar, MapInput, MapOutput, NyqStab, or Phase Noise.

For example, the simulation that determines oscillator frequency of a Clapp oscillator has the design filename *ClappFixedFreqOsc.dsn* and a data display filename *ClappFixedFreqOsc.dds*. This simulation predicts oscillation frequency, output power, and calculates tank components  $L_t$  and  $C_t$ . The accompanying data display presents these results.

The *cClappCore* circuit is used to illustrate available simulation setups offered in the design guide.

## Large Signal Oscillations

Oscport determines the steady-state oscillations.



Harmonic Balance  
HB1  
Freq[1]=fo  
Order[1]=7

VAR  
VAR4  
fo=1 GHz  
Vcc=5V

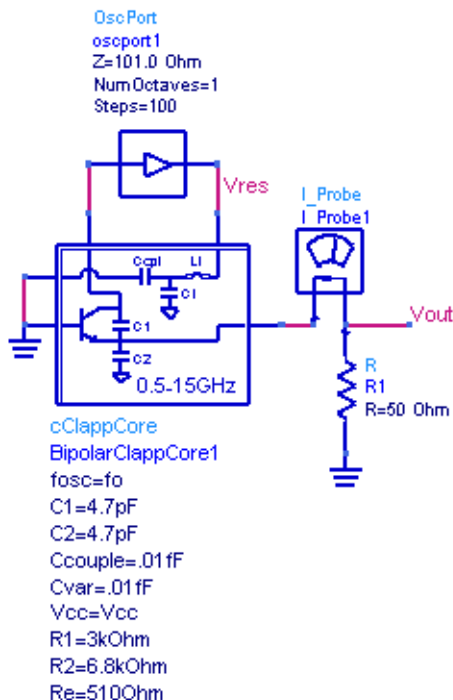


Figure 2-24. Oscillator Core Circuit Simulation Connections

Figure 2-24 shows the Clapp oscillator harmonic balance simulation *ClappFixedFreqOsc.dsn* that determines oscillation frequency, output power and tank component values at 1GHz. The *OscPort* probe is shown connected between the active device and tank resonator. Resonator tank components  $L_t$  and  $C_t$  are computed and shown on the companion data display page.

## Oscillator Core Series Resonator Connection Alternatives

Oscillator core circuits (cClappCore, cHartleyCore, cModifiedClappCore, and cModifiedColpittsCore) are compatible with series resonators in two ways.

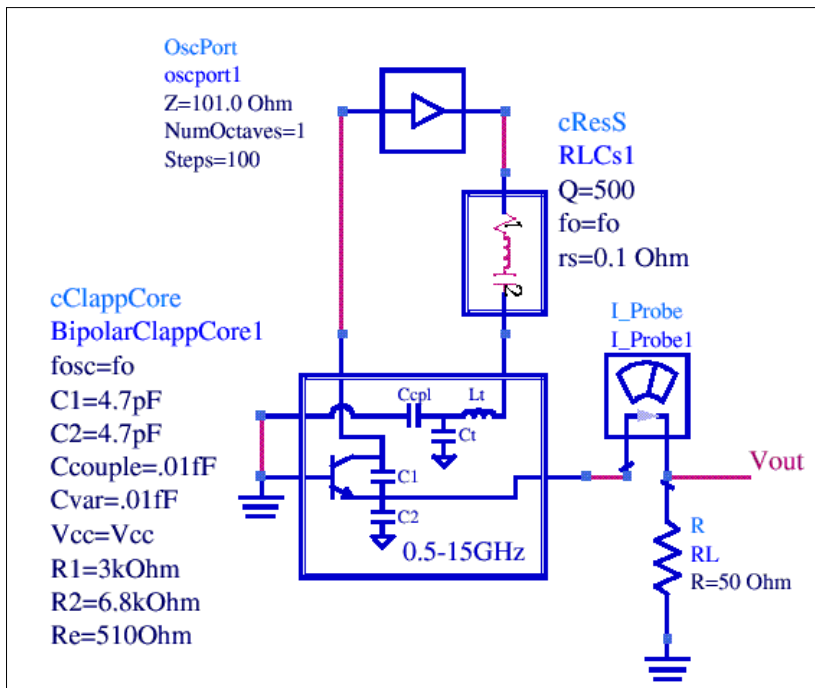


Figure 2-25. Series Resonator Connection Between Tank and Active Circuit

The first possibility shown by [Figure 2-25](#) connects the series resonator between the tank circuit and active device. If resonator losses are low, the series resonator can also be connected between the bipolar base terminal and ground as shown in [Figure 2-26](#).

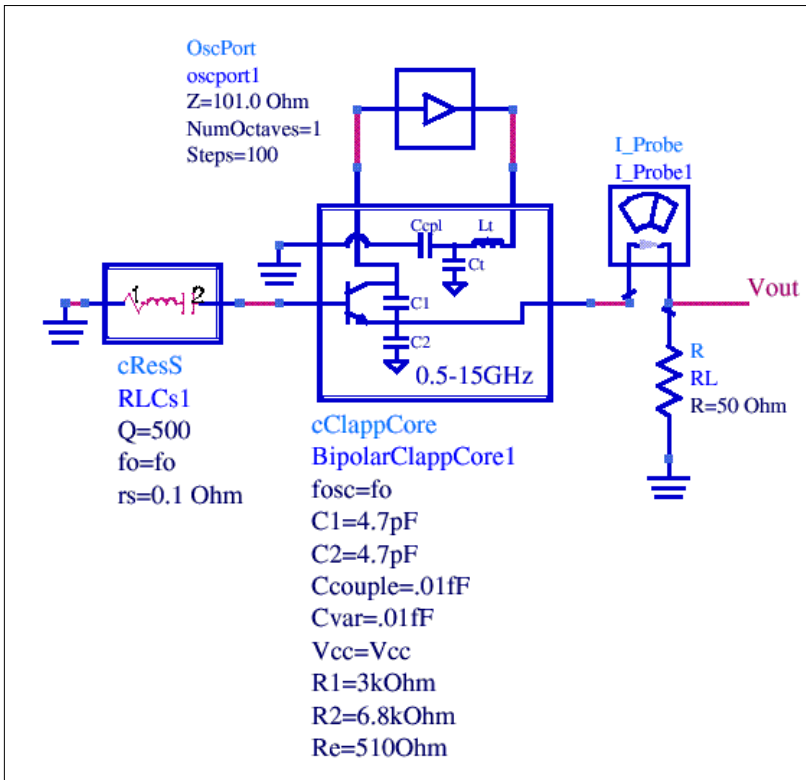


Figure 2-26. Series Resonator Connection Transistor Base and Ground

## Additional Examples

Following are examples in addition to the primary example, as described in the section, “[Generic Oscillator Example](#)” on page 2-18.

The design and display filenames for these examples follow the generic oscillator naming convention with 3-letter prefixes attached to the generic names, as follows:

- xxxgenericoscillator name.dsn
- xxxgenericoscillator name.dds

where xxx stands for one of: saw, vco, xto, or yto.

For example, VCO Large Signal S-Parameters have the filenames *vcoLSSpar.dsn* and *vcoLSSpar.dds*.

## Crystal Oscillator (XTO)

These oscillators are notable for their high frequency stability and low cost. Typical structure is that of a Colpitts oscillator with quartz crystal resonator introduced into feedback path. Mechanical vibrations of the crystal stabilize the oscillations frequency. Vibration frequency is sensitive to temperature. Therefore, temperature compensation circuits are often used to improve frequency stability. Crystal resonators are typically used in the range up to 100 MHz (to a few hundreds of MHz if resonating on overtones).

## SAW Resonator Oscillator (SAW)

Principle of operation is similar to that of crystal oscillator with the quartz resonator replaced by a Surface Acoustic Wave oscillator. SAW resonators are used in frequency range up to 2 GHz.

## Voltage Controlled Oscillator (VCO)

In any of the preceding structures, frequency tuning can be provided by adding a varactor diode to the resonator. The varactor diode serves as a voltage controlled capacitor. It has very fast tuning speed (GHz/nsec) and low Q. Consequently, the varactor can be used with LC elements to provide wide tuning (with poor frequency stability) or with a crystal, SAW or DRO resonator for narrow tuning with better frequency stability.

At microwave frequencies, the device capacitances become significant, resulting in a different (often simpler) circuit. The operation principles, remain the same.

## **YIG Tuned Oscillator (YTO)**

For a very wide band (that can reach decade) tuning with high frequency stability and for frequency range of 1 GHz to 50 GHz. YIG (Yttrium-Iron-Garnett) resonators are used. The YIG sphere behaves like a resonator with 1000-to-8000 unloaded  $Q$  resulting in very good frequency stability. The resonator are tunable over wide bandwidth with excellent linearity ( $\sim 0.05\%$ ). For fine tuning (for phase-lock), or frequency modulation an FM coil can be added.

## Bibliography

- [1] G.R.Basawapatna, R.B.Stancliff, "A Unified Approach to the Design of Wide-Band Microwave Solid State Oscillators," IEEE Trans. MTT-27, May 1979, pp 379-385.
- [2] Fundamentals of Quartz Oscillators" HP Application Note 200-2.
- [3] R. T. Jackson, "Criteria for the Onset of Oscillations in Microwave Circuits," IEEE Trans. MTT-40, Mar 1992, pp 566-568, "Comments on ``Criteria...," IEEE Trans. MTT-40, Sept 1992, pp1850-1851.
- [4] A. P. S. Khanna, "Oscillators" in Chapter 9 of "Microwave Solid State Circuit Design" by I. Bahl P. Bhartia, J.Wiley 1988.
- [5] Khanna and Obregon - "Microwave Oscillator Analysis" - IEEE Trans.vol. MTT-29, June 1981, pp 606-607.
- [6] M.Odyniec, "Nonlinear Synchronized LC Oscillators, Theory and Simulation," IEEE Transactions, MTT-41, May 1993, pp 774-780.
- [7] M.Odyniec, "Benchmarks for Nonlinear Simulation," APMC'94, Tokyo, Japan, Dec. 6-9 1994.
- [8] M.Odyniec, "Stability Criteria via S-parameters," EUMC'95, Bologna, Italy, Sept. 4-8, 1995.
- [9] V.Rizzoli, F. Mastri, and D, Maroti, "General noise analysis," IEEE Trans. MTT-37, Vol.1, No. 1, January 1989.
- [10] C. Schiebold, "Getting Back to the Basics of Oscillator Design," Microwave Journal, May 1998.
- [11] J.C. Slater, "Microwave Electronics," Van Nostrand, Princeton, 1950.
- [12] "S-parameter Design," HP Application Note 159.
- [13] A. Sweet, "MIC and MMIC Amplifier and Oscillator Circuit Design," Artech House, Boston 1990.
- [14] D. Warren et al., "Large and Small Signal Oscillator Analysis," Microwave Journal, May 1989.
- [15] J.J. D'Azzo, C. H. Houpis, "Feedback Control Systems," McGraw-Hill New York 1960.

- [16] J. W. Boyles, "The Oscillator as a Reflection Amplifier..." Microwave Journal, June 1986.
- [17] J.L.J. Martin and F.J.O.Gonzalez, "Accurate Linear Oscillator Analysis and Design," Microwave Journal, June 1996.
- [18] Z. Nativ and Y. Shur, "Push-Push VCO Design with CAD Tools. " Microwave Journal, February 1989.
- [19] M. Odyniec, "Stability Criteria via S-parameters "European Microwave Conference, Bologna, Italy, September 1995.
- [20] A. Przedpelski, "Simple, Low Cost UHF VTOs "RF Design," May 1993.
- [21] N. Ratier et al. "Automatic Formal Derivation of the Oscillation Condition", IEEE Int. Frequency Control Symp. 1997.
- [22] T. Razban et al., "A Compact Oscillator..." Microwave Journal, February 1994.
- [23] S. Savaria and P. Champagne, "Linear Simulators..." Microwave Journal, May 1995.

## **Selected Papers**

- [25] Basawapatna and Stancliff - "A Unified Approach to the Design of Wideband Microwave Solid State Oscillators" - IEEE trans. vol MTT-27, May 1979, pp 379-385.
- [26] Khanna and Obregon - "Microwave Oscillator Analysis" - IEEE Trans. vol. MTT-29, June 1981, pp 606-607.
- [27] M.Odyniec, "Nonlinear Synchronized LC Oscillators, Theory and Simulation," IEEE Transactions, MTT-41, May 1993, pp 774-780.
- [28] M.Odyniec, "Benchmarks for Nonlinear Simulation," APMC'94, Tokyo, Japan, Dec. 6-9 1994.
- [29] M.Odyniec, "Stability Criteria via S-parameters," EUMC'95, Bologna, Italy, Sept. 4-8, 1995.



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